

Model Name: GA-H61M-S2P-GQ

SHEET TITLE

01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1155-A
05	CPU_LGA1155-B
06	CPU_LGA1155-C
07	DDR III CHANNEL A
08	DDR III CHANNEL B
09	PCH_FDI,DMI,USB,PCIE,NVRAM
10	PCH_DP,CLK BUFFER
11	PCH_HOST,SATA,PCI
12	PCH_GPIO,CTRL,AUDIO
13	PCH_PWR,GND
14	PCI EXPRESS*16 SLOT
15	PCI EXPRESS*1 SLOT
16	IT8892E
17	PCI SLOT1&2
18	ITE 8728 LPC IO
19	COM,KB_USB,USB_ESATA,-PROCHOT
20	HWM,FAN CTRL,OV,
21	DUAL BIOS
22	FP,FUSB,SPK,SATALED
23	Realtek ALC887-VD2
24	REAR AUDIO JACK
25	Artheros AR8151
26	DISCRETE POWER
27	ATX

SHEET TITLE

28	RT8120_CPU_VTT
29	VCORE ISL95836_1
30	VCORE ISL95836_2
31	VCORE ISL95836_3
32	LPT

Gigabyte Technology			
Title			
Cover Sheet			
Size	Document Number	GA-H61M-S2P-GQ	Rev
Custom			1.0
Date:	Friday, July 06, 2012	Sheet	1 of 32

Model Name: GA-H61M-S2P-GQ

Component value change history

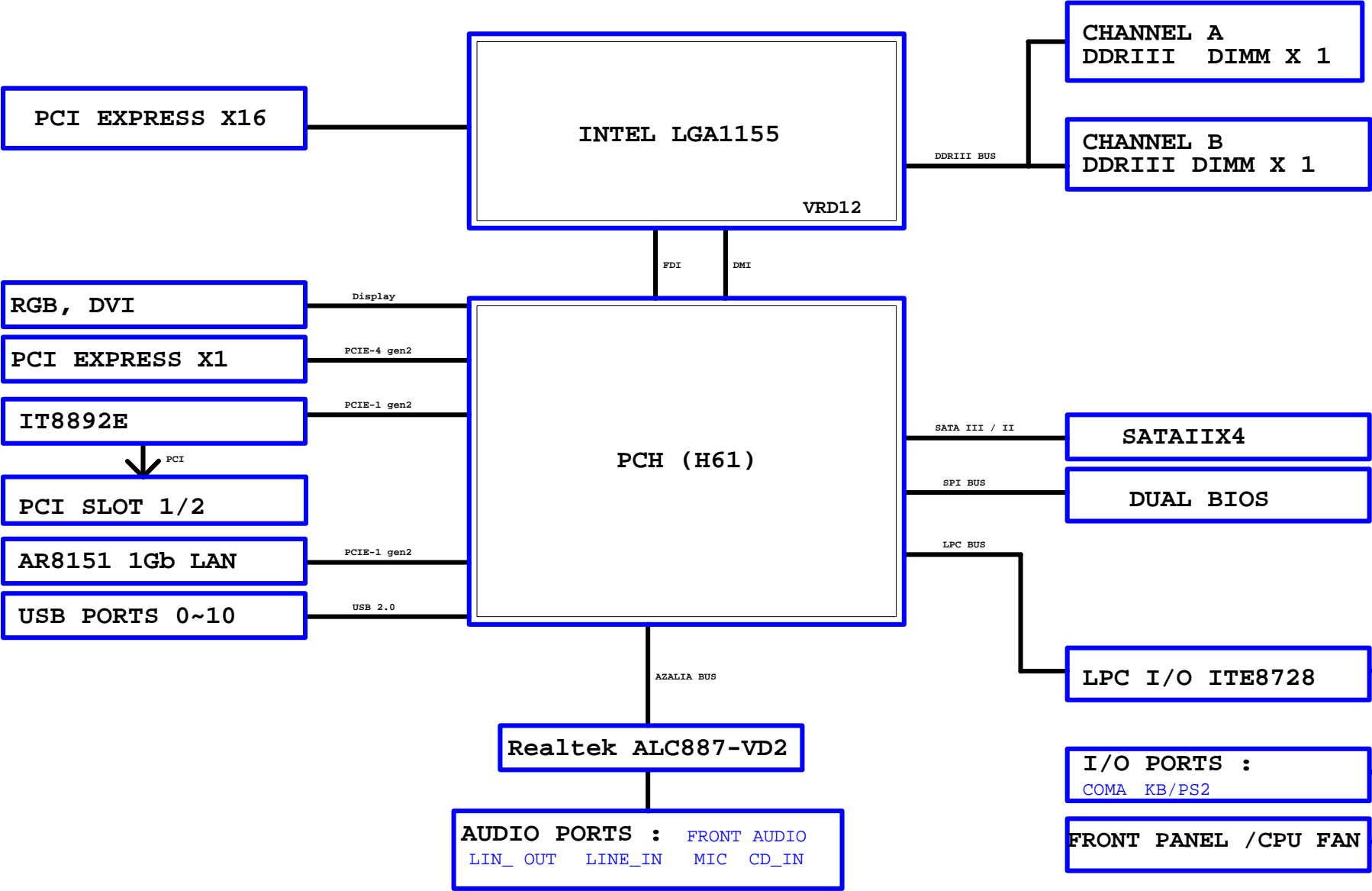
2011/12/21

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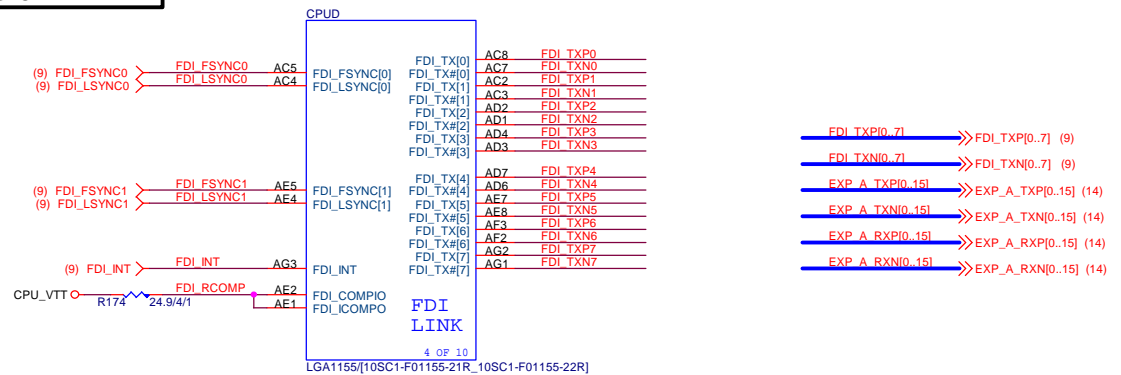
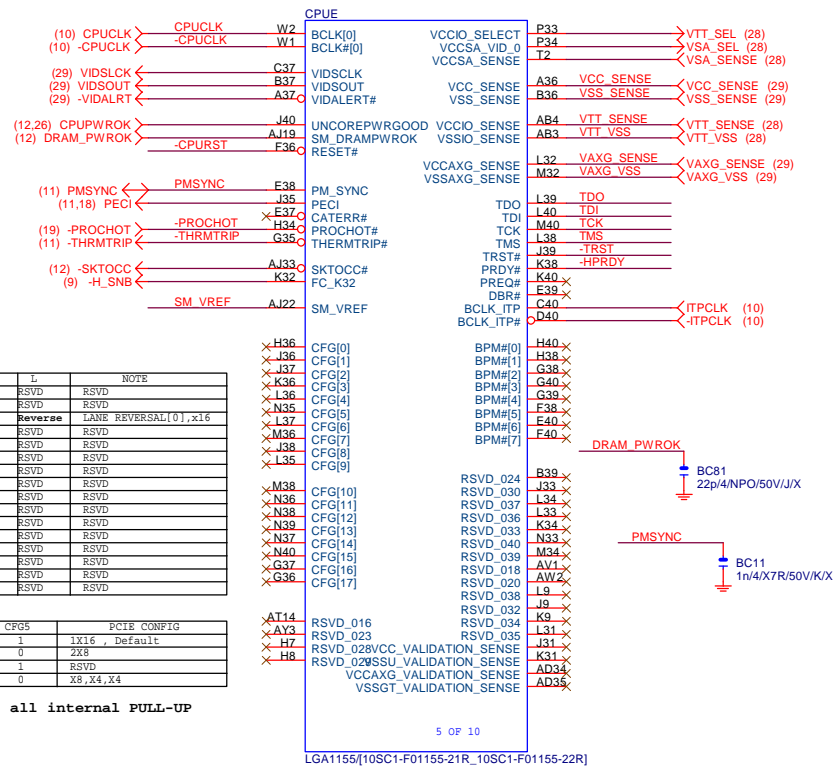
Circuit or PCB layout change

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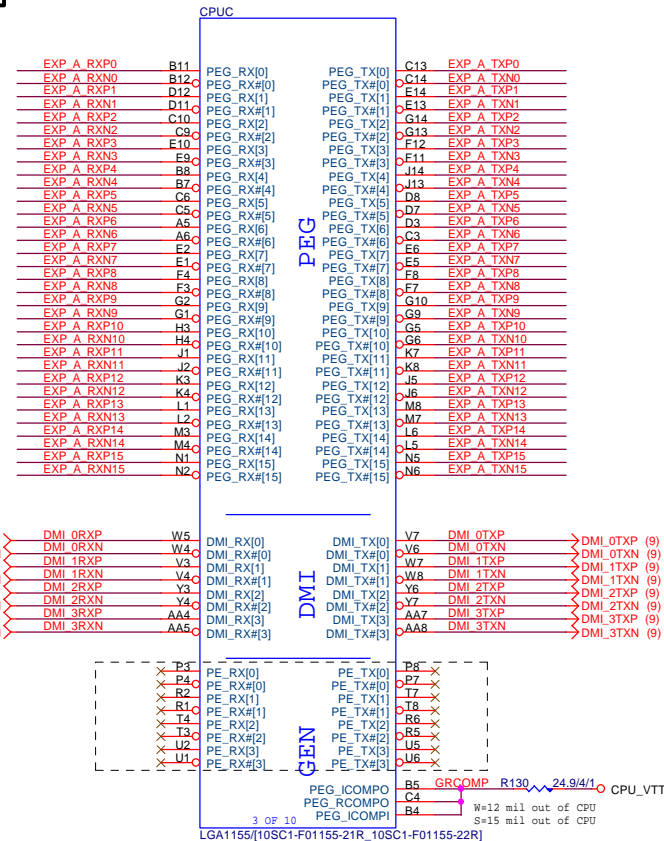
BLOCK DIAGRAM



CPU E



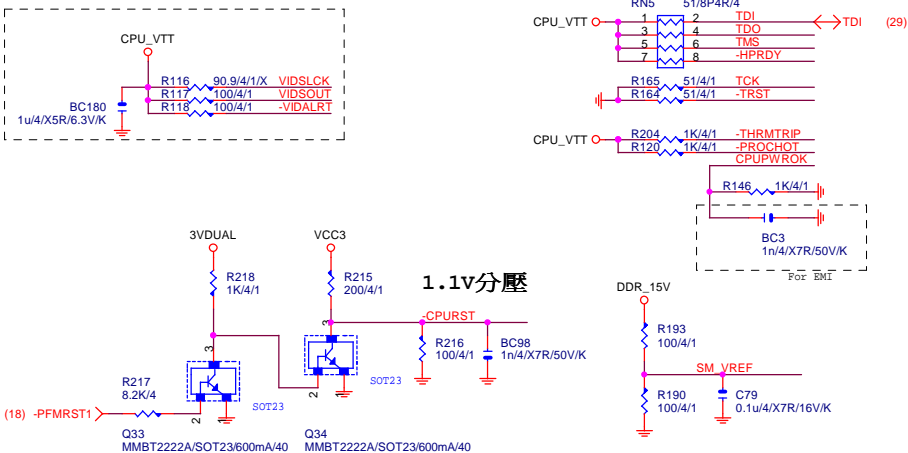
CPU C



STITCHING CAP.

N/A

Stitching caps for PCIE,DMI,FDI bus



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Title			
CPU LGA1155-A			
Size	Document Number		Rev
Custom	GA-H61M-S2P-GQ		1.0
Date: Friday, July 06, 2012		Sheet 4 of 32	

CPU A

CPUA			
MAAA0	AW27	SA_MAJ[0]	SA_DQS[0] AK3 DQSA0
MAAA1	AW24	SA_MAJ[1]	SA_DQS# [0] AK2 -DQSA0
MAAA2	AW24	SA_MAJ[2]	
MAAA3	AW23	SA_MAJ[3]	
MAAA4	AW23		AJ3 MDA0
MAAA5	AT24	SA_MAJ[4]	AJ4 MDA1
MAAA6	AT23	SA_MAJ[5]	AJ3 MDA2
MAAA7	AU22	SA_MAJ[6]	AL4 MDA3
MAAA8	AU22	SA_MAJ[7]	AJ2 MDA4
MAAA9	AT22	SA_MAJ[8]	AJ1 MDA5
MAAA10	AU28	SA_MAJ[9]	AL2 MDA6
MAAA11	AU21	SA_MAJ[10]	AL1 MDA7
MAAA12	AT21	SA_MAJ[11]	
MAAA13	AW32	SA_MAJ[12]	AP3 DQSA1
MAAA14	AU20	SA_MAJ[13]	AP2 -DQSA1
MAAA15	AT20	SA_MAJ[14]	
		SA_MAJ[15]	
(7) -SWEA < -SWEA	AW29	SA_WE#	AN1 MDA8
(7) -SCASA < -SCASA	AW30	SA_CAS#	AN4 MDA9
	AU28	SA_RAS#	AR3 MDA10
			AR4 MDA11
(7) SBA0 < SBA0	AY29	SA_BS[0]	SA_DQ[11] AN2 MDA12
(7) SBA1 < SBA1	AW28	SA_BS[1]	SA_DQ[12] AR3 MDA13
(7) SBA2 < SBA2	AW20	SA_BS[2]	SA_DQ[13] AN2 MDA14
			SA_DQ[14] AR1 MDA15
			SA_DQ[15]
(7) -CSA0 < -CSA0	AU29	SA_CSM[0]	
(7) -CSA1 < -CSA1	AW32	SA_CSM[1]	SA_DQ[2] AW4 DQSA2
	AW30	SA_CSM[2]	-DQSA2
	AW33	SA_CSM[3]	
(7) CKEA0 < CKEA0	AV19	SA_CKE[0]	AV2 MDA16
(7) CKEA1 < CKEA1	AT19	SA_CKE[1]	AV3 MDA17
	AU18	SA_CKE[2]	AV5 MDA18
	AW18	SA_CKE[3]	AW5 MDA19
			AU2 MDA20
			AJ3 MDA21
			AU5 MDA22
			SA_DQ[22] AYS MDA23
			SA_DQ[23]
			SA_DQS[3] AV8 DQSA3
			SA_DQS# [3] AW8 -DQSA3
(7) DCLKA0 < DCLKA0	AY25	SA_CK[0]	
(7) DCLKA0 < DCLKA0	AW25	SA_CK# [0]	SA_DQ[24] AY7 MDA24
(7) DCLKA1 < DCLKA1	AU24	SA_CK[1]	SA_DQ[25] AU7 MDA25
(7) DCLKA1 < DCLKA1	AU25	SA_CK# [1]	SA_DQ[26] AV9 MDA26
	AW27	SA_CK[2]	SA_DQ[26] AV9 MDA27
	AW27	SA_CK# [2]	AU7 MDA28
	AW26	SA_CK[3]	SA_DQ[27] AW7 MDA29
	AW26	SA_CK# [3]	SA_DQ[28] AW3 MDA30
			SA_DQ[29] AV7 MDA31
			SA_DQ[30] AY3 MDA32
			SA_DQ[31]
(7,8) -DDR3_RST < -DDR3_RST	AW18	SM_DRAMRST#	
			SA_DQS[4] AV9 DQSA4
			SA_DQS# [4] AV36 -DQSA4
			SA_DQ[32] AU35 MDA32
			SA_DQ[33] AW37 MDA33
			SA_DQ[34] AU39 MDA34
			SA_DQ[35] AU36 MDA35
			SA_DQ[36] AW35 MDA36
			SA_DQ[37] AY36 MDA37
			SA_DQ[38] AU38 MDA38
			SA_DQ[39] AU37 MDA39
			SA_DQS[5] AP38 DQSA5
			SA_DQS# [5] AP39 -DQSA5
			SA_DQ[40] AR40 MDA40
			SA_DQ[41] AR37 MDA41
			SA_DQ[42] AL37 MDA42
			SA_DQ[43] AN37 MDA43
			SA_DQ[44] AR39 MDA44
			SA_DQ[45] AL38 MDA45
			SA_DQ[46] AN39 MDA46
			SA_DQ[47] AN40 MDA47
			SA_DQS[6] AK38 DQSA6
			SA_DQS# [6] AK39 -DQSA6
			SA_DQ[48] AL40 MDA48
			SA_DQ[49] AL37 MDA49
			SA_DQ[50] AJ38 MDA50
			SA_DQ[51] AJ37 MDA51
			SA_DQ[52] AL39 MDA52
			SA_DQ[53] AJ39 MDA53
			SA_DQ[54] AJ38 MDA54
			SA_DQ[55] AJ40 MDA55
			SA_DQS[7] AF38 DQSA7
			SA_DQS# [7] AF39 -DQSA7
			SA_DQ[56] AG40 MDA56
			SA_DQ[57] AG37 MDA57
			SA_DQ[58] AE38 MDA58
			SA_DQ[59] AE37 MDA59
			SA_DQ[60] AG39 MDA60
			SA_DQ[61] AJ38 MDA61
			SA_DQ[62] AE39 MDA62
			SA_DQ[63] AE40 MDA63

LGA1155/[10SC1-F01155-21R_10SC1-F01155-22R]

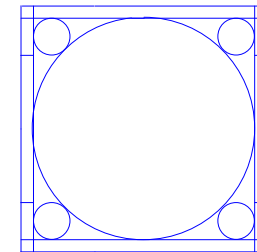
CPU B

CPU0				CPU1			
	MAAB0	AK24	SB_MA[0]	SB_DQS[0]	AH7	DQS80	
	MAAB1	AM20	SB_MA[1]	SB_DQS[0]	AH6	DQS80	
	MAAB2	AM19	SB_MA[2]				
	MAAB3	AK18	SB_MA[3]				
	MAAB4	AP19	SB_MA[4]	SB_DQ[0]	AG7	MD80	
	MAAB5	AP18	SB_MA[5]	SB_DQ[1]	AG8	MD81	
	MAAB6	AM18	SB_MA[6]	SB_DQ[2]	AJ9	MD82	
	MAAB7	AL18	SB_MA[7]	SB_DQ[3]	AA8	MD83	
	MAAB8	AN18	SB_MA[8]	SB_DQ[4]	AG5	MD84	
	MAAB9	AY17	SB_MA[9]	SB_DQ[5]	AG6	MD85	
	MAAB10	AN23	SB_MA[10]	SB_DQ[6]	AJ6	MD86	
	MAAB11	AU17	SB_MA[11]	SB_DQ[7]	AJ7	MD87	
	MAAB12	AT18	SB_MA[12]				
	MAAB13	AR26	SB_MA[13]	SB_DQS[1]	AM8	DQS81	
	MAAB14	AY16	SB_MA[14]	SB_DQS[1]	AL8	DQS81	
	MAAB15	AV16	SB_MA[15]				
(8)	-SWEB	-SWEB	AR25	SB_WE#	SB_DQ[8]	AL7	MD88
(8)	-SCASB	-SCASB	AR25	SB_CAS#	SB_DQ[9]	AM7	MD89
(8)	-SRASB	-SRASB	AP24	SB_RAS#	SB_DQ[10]	AM10	MD90
(8)	SBAB0	SBAB0	AP23	SB_BS[0]	SB_DQ[11]	AL10	MD91
(8)	SBAB1	SBAB1	AM24	SB_BS[1]	SB_DQ[12]	AM6	MD13
(8)	SBAB2	SBAB2	AW17	SB_BS[2]	SB_DQ[14]	AL9	MD14
				SB_DQ[15]	AM9	MD15	
(8)	-CSB0	-CSB0	AN25	SB_CS[0]			
(8)	-CSB1	-CSB1	AN26	SB_CS[1]	SB_DQS[2]	AR8	DQS82
		AL25	SB_CS[2]	SB_DQS[2]	AP8	DQS82	
		AL26	SB_CS[3]				
		AL26	SB_CS[4]				
(8)	CKEB0	CKEB0	AU16	SB_CKE[0]	SB_DQ[16]	AP7	MD16
(8)	CKEB1	CKEB1	AY15	SB_CKE[1]	SB_DQ[17]	AR7	MD17
		AY15	SB_CKE[2]	SB_DQ[18]	AP10	MD18	
		AY15	SB_CKE[3]	SB_DQ[19]	AR10	MD19	
		AL23	SB_DQ[20]	SB_DQ[20]	AP6	MD20	
	MODT_B0	AL26	SB_ODT[0]	SB_DQ[21]	AR6	MD21	
	MODT_B1	AP26	SB_ODT[1]	SB_DQ[22]	AP9	MD22	
		AM26	SB_ODT[2]	SB_DQ[23]	AR9	MD23	
		AK26	SB_ODT[3]				
			SB_DQS[3]	SB_DQS[3]	AN13	DQS83	
			SB_DQS[3]	SB_DQS[3]	AN12	DQS83	
(8)	DCLKB0	DCLKB0	AL21	SB_CK[0]			
(8)	DCLKB0	DCLKB0	AL22	SB_CK[0]	SB_DQ[24]	AM12	MD24
(8)	DCLKB1	DCLKB1	AL20	SB_CK[1]	SB_DQ[25]	AM13	MD25
(8)	DCLKB1	DCLKB1	AK20	SB_CK[1]	SB_DQ[26]	AR11	MD26
		AM22	SB_CK[2]	SB_DQ[27]	AP13	MD27	
		AP21	SB_CK[2]	SB_DQ[28]	AL12	MD28	
		AP21	SB_CK[3]	SB_DQ[29]	AL13	MD29	
		AN21	SB_CK[4]	SB_DQ[30]	AR12	MD30	
			SB_CK[4]	SB_DQ[31]	AP12	MD31	
			SB_DQS[4]	SB_DQS[4]	AN29	DQS84	
			SB_DQS[4]	SB_DQS[4]	AN28	DQS84	
	VREF_DQB	AH4	FC_AH1				
	VREF_DQA	AH4	FC_AH4				
				SB_DQ[32]	AR28	MD32	
				SB_DQ[33]	AR29	MD33	
				SB_DQ[34]	AL28	MD34	
				SB_DQ[35]	AL29	MD35	
				SB_DQ[36]	AP28	MD36	
				SB_DQ[37]	AP29	MD37	
	AN16		SB_DQS[8]	AM28	MD38		
	AN15		SB_DQS[8]	AM29	MD39		
			SB_DQS[5]	SB_DQ[39]			
			SB_DQS[5]		AP33	DQS85	
			SB_DQS[5]		AR33	DQS85	
	AL16	SB_ECC_CB[0]					
	AM16	SB_ECC_CB[1]					
	AP16	SB_ECC_CB[2]					
	AR16	SB_ECC_CB[3]	SB_DQ[40]		AP32	MD840	
	AL15	SB_ECC_CB[4]	SB_DQ[41]		AP31	MD841	
	AM15	SB_ECC_CB[5]	SB_DQ[42]		AP35	MD842	
	AR15	SB_ECC_CB[6]	SB_DQ[43]		AP34	MD843	
	AP15	SB_ECC_CB[7]	SB_DQ[44]		AR32	MD844	
			SB_DQ[45]		AR35	MD845	
			SB_DQ[46]		AR34	MD846	
			SB_DQ[47]		AR34	MD847	
			SB_DQS[6]	SB_DQS[6]	AL33	DQS86	
			SB_DQS[6]	SB_DQS[6]	AM33	DQS86	
			SB_DQ[48]		AM32	MD848	
			SB_DQ[49]		AL31	MD849	
			SB_DQ[50]		AL35	MD850	
			SB_DQ[51]		AL32	MD851	
			SB_DQ[52]		AM34	MD852	
			SB_DQ[53]		AL31	MD853	
			SB_DQ[54]		AM35	MD854	
			SB_DQ[55]		AL34	MD855	
			SB_DQS[7]				
			SB_DQS[7]		AG35	DQS87	
			SB_DQS[7]		AG34	DQS87	
			SB_DQ[56]				
			SB_DQ[57]		AH35	MD856	
			SB_DQ[58]		AH34	MD857	
			SB_DQ[59]		AE34	MD858	
			SB_DQ[60]		AE35	MD859	
			SB_DQ[61]		AJ35	MD860	
			SB_DQ[62]		AJ34	MD861	
			SB_DQ[63]		AF33	MD862	
			SB_DQ[64]		AF35	MD863	

DDR 1

LGA1155/[10SC1-F01155-21R_10SC1-F01155-22R]

CPU SOCKET

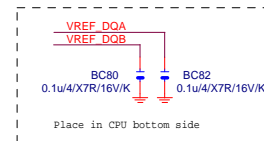
CR
ORLDETATIONX

Need check the new CPU ME



ILM_BP/1156/CSP/ILM_BP/1156/CSP/[12KRC-0F0001-05R_12KRC-0F0001-31R]

DDR SIGNAL



(7) MODT A[0..1] \leftrightarrow MODT A[0..1]

(8) $\text{MODT_B}[0..1] \leftrightarrow \text{MODT_B}[0..1]$

(7) MDA[0..63] \longleftrightarrow MDA[0..63]

(8) $\text{MDB}[0..63] \longleftrightarrow \text{MDB}[0..63]$

(7) DQSA[0..7] \leftrightarrow DQSA[0..7]

(7) -DQSA[0..7] \longleftrightarrow -DQSA[0..7]

(7) $MAAA[0..15] \longleftrightarrow MAAA[0..15]$

(8) MAAB[0..15] \longleftrightarrow MAAB[0..15]

(8) DQSB[0..7] \longleftrightarrow  DQSB[0..7]

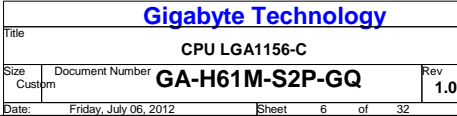
(8) $-DQSB[0..7] \leftrightarrow -DQSB[10..17]$

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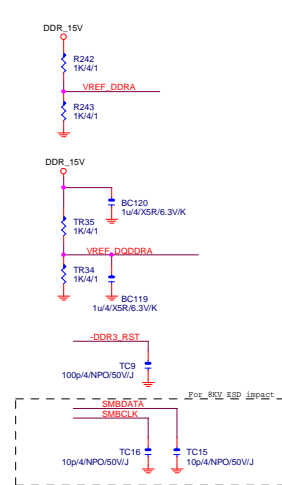
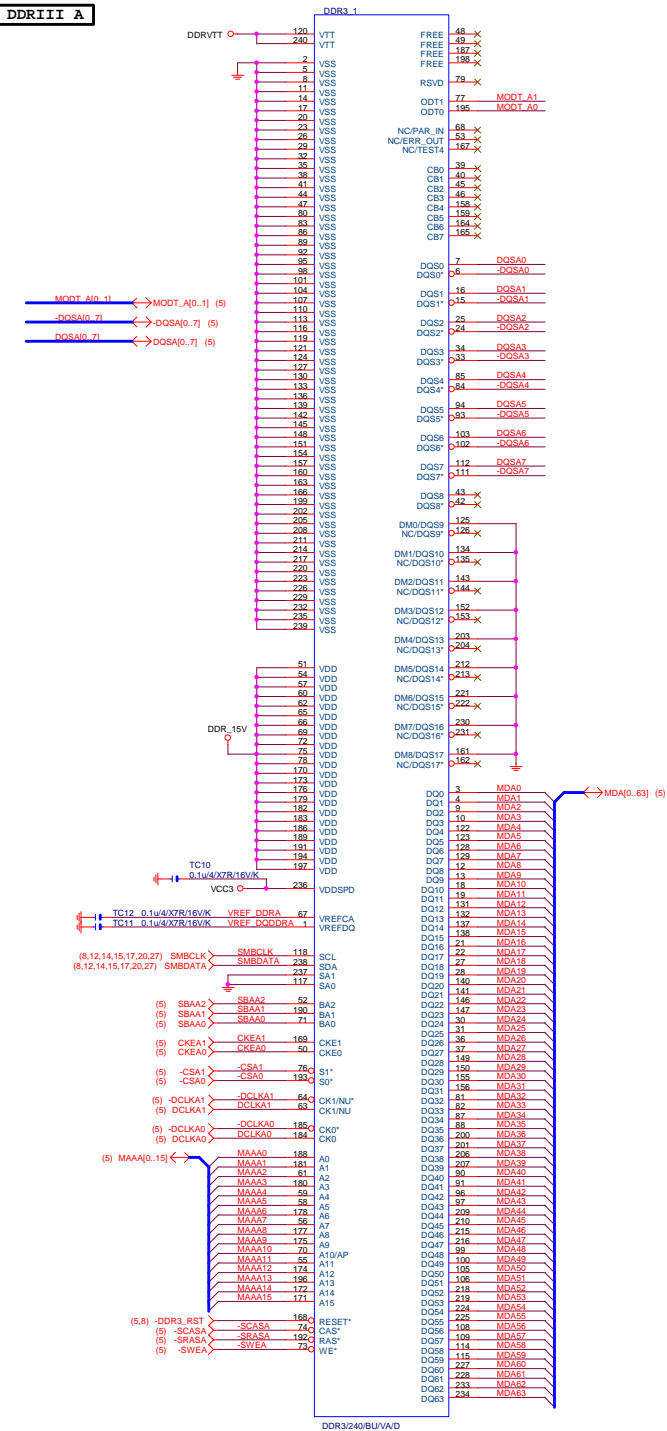
CPU LGA1156-B

Size Custom	Document Number GA-H61M-S2P-GQ	Rev 1.0
Date:	Friday, July 06, 2012	Sheet 5 of 32

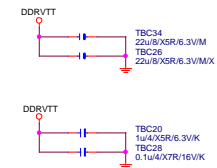
CPU I,J GND



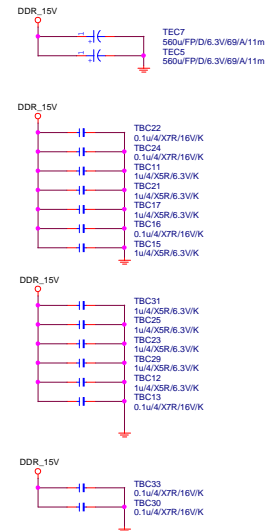
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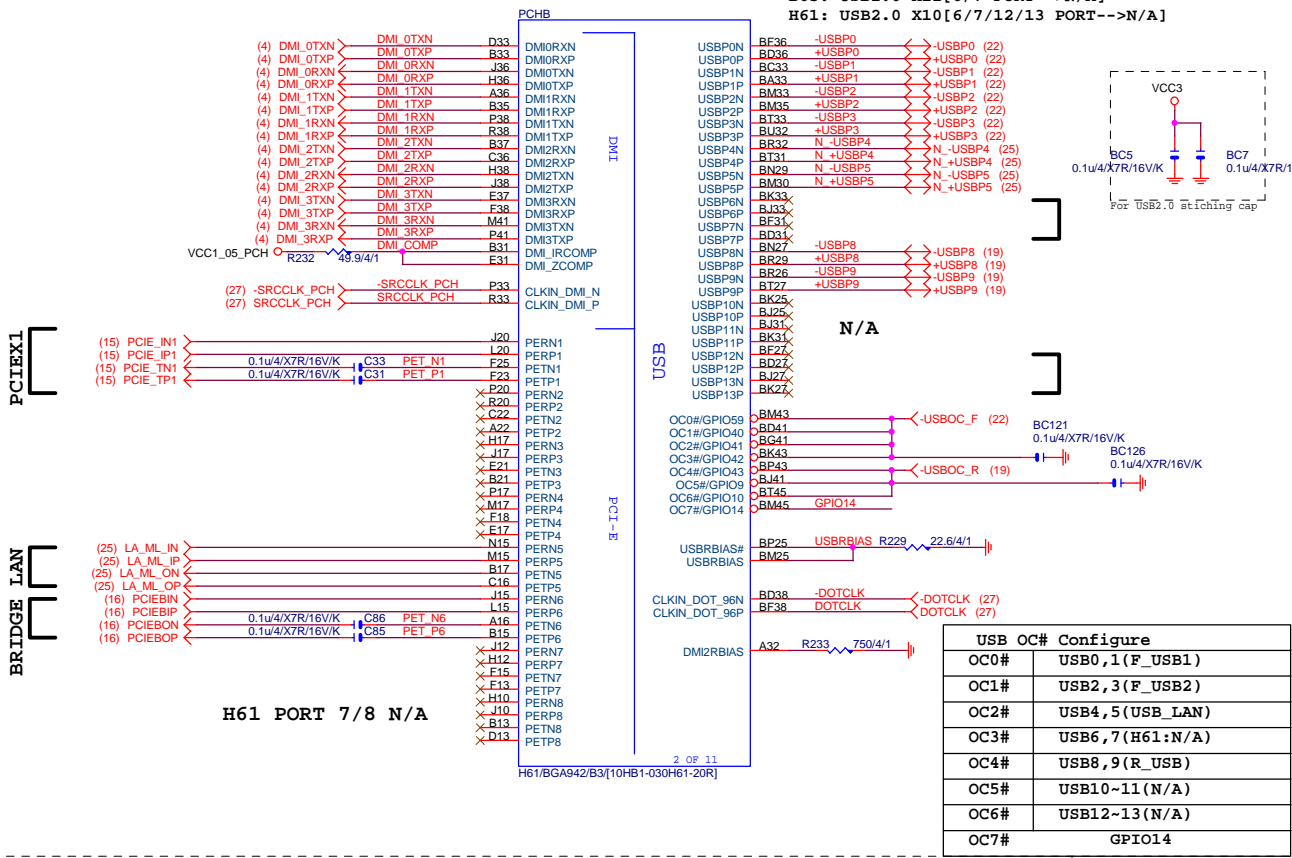
DDRVTT Decouple



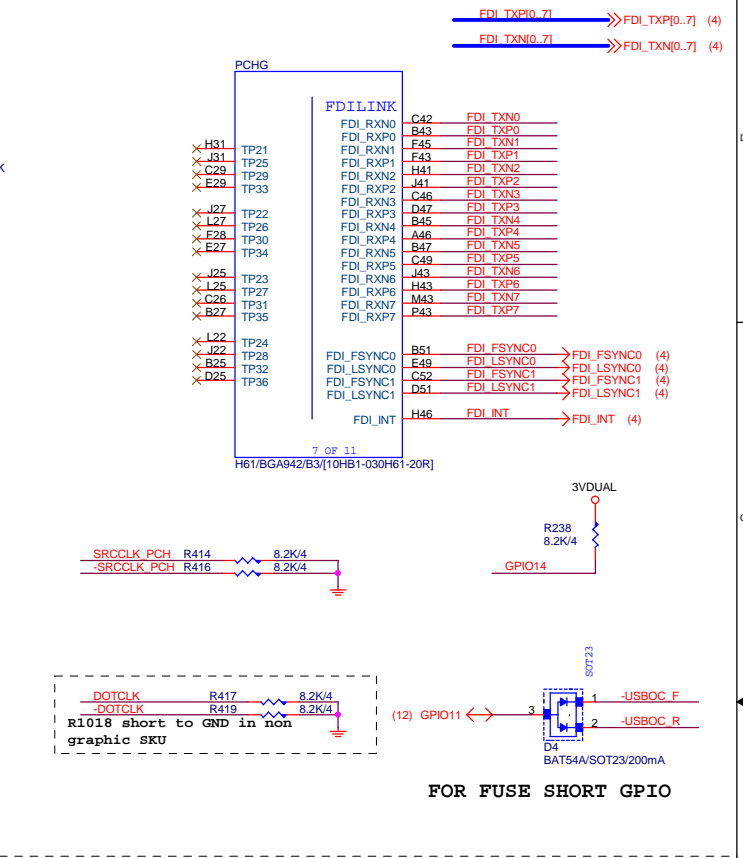
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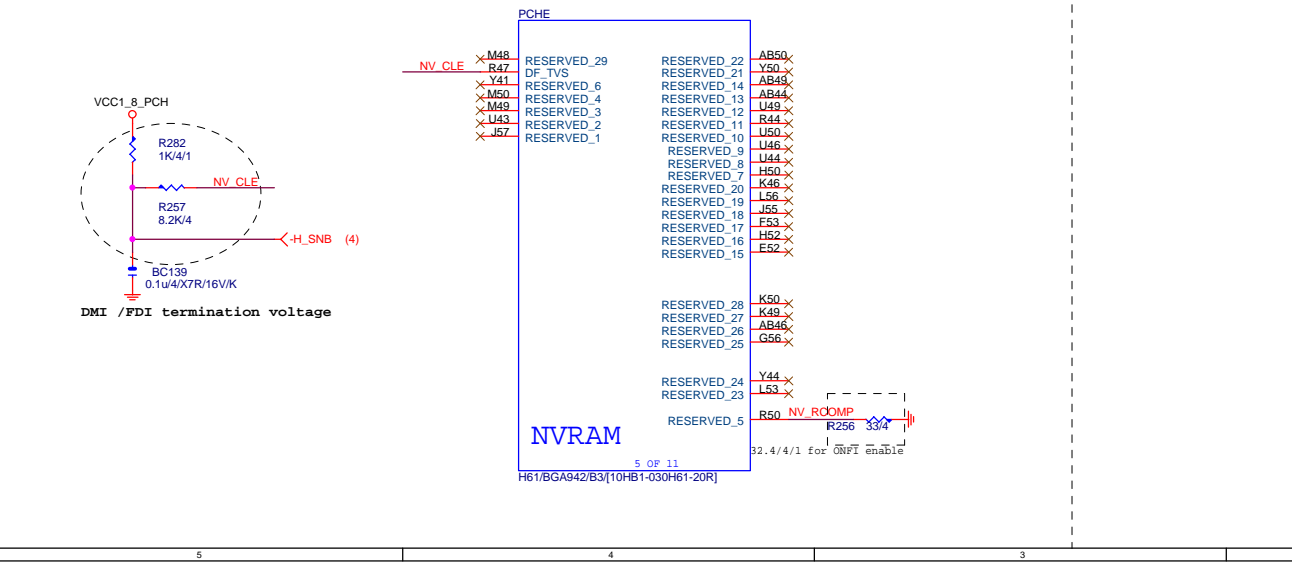
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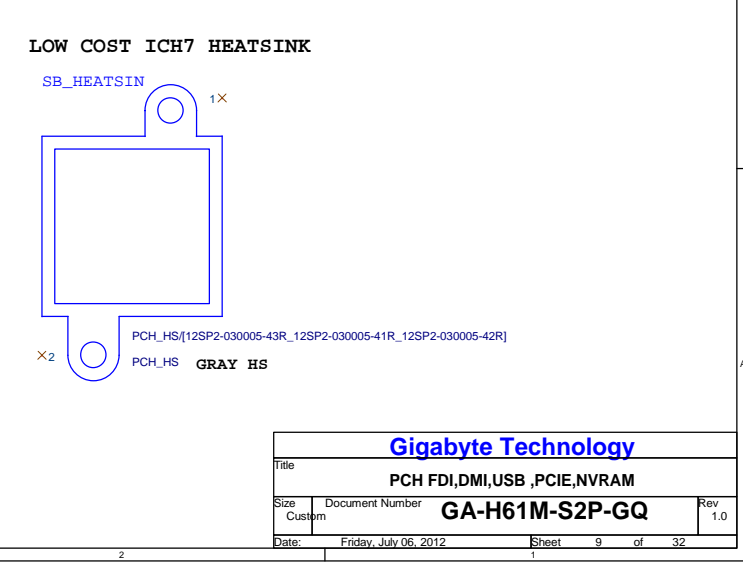
PCH G FDI



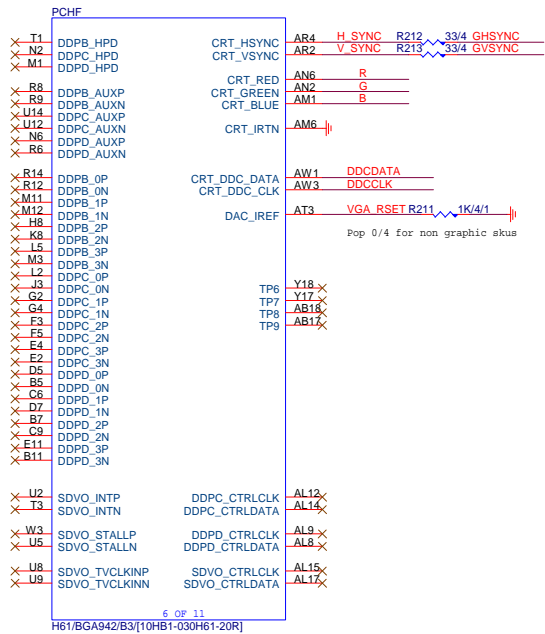
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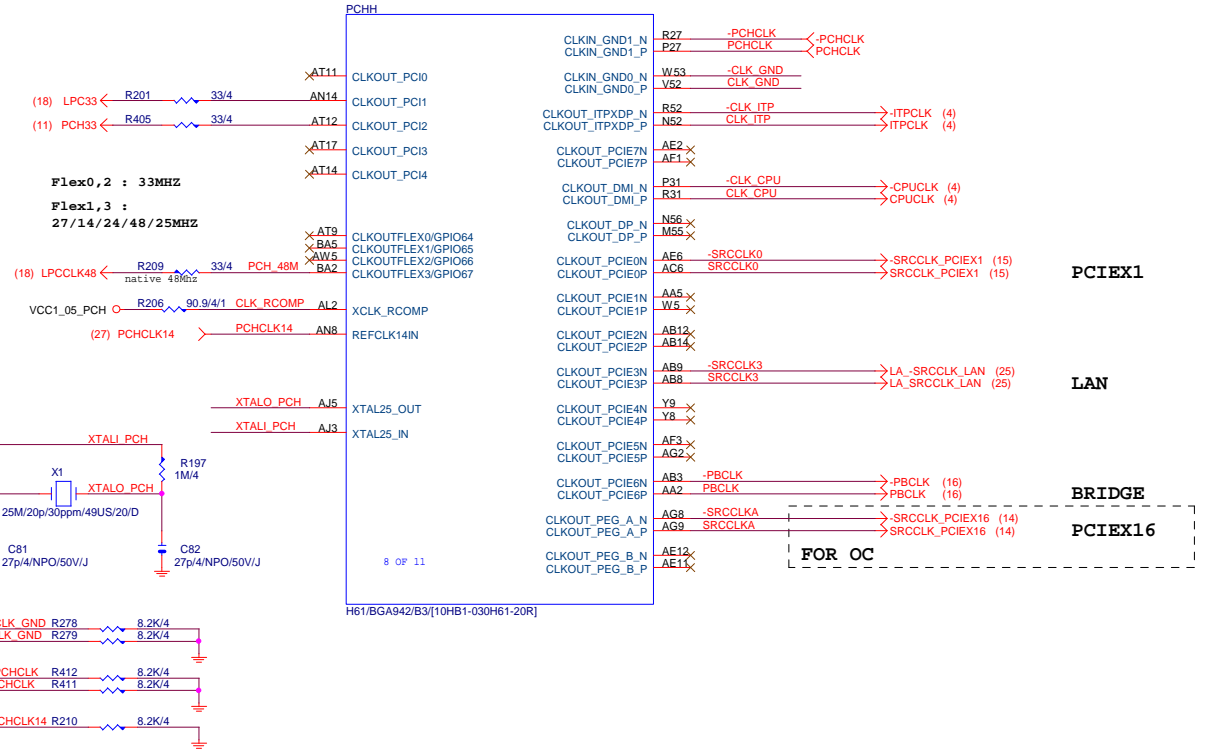
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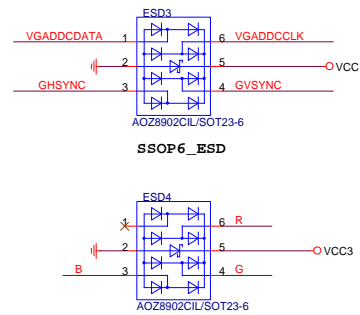
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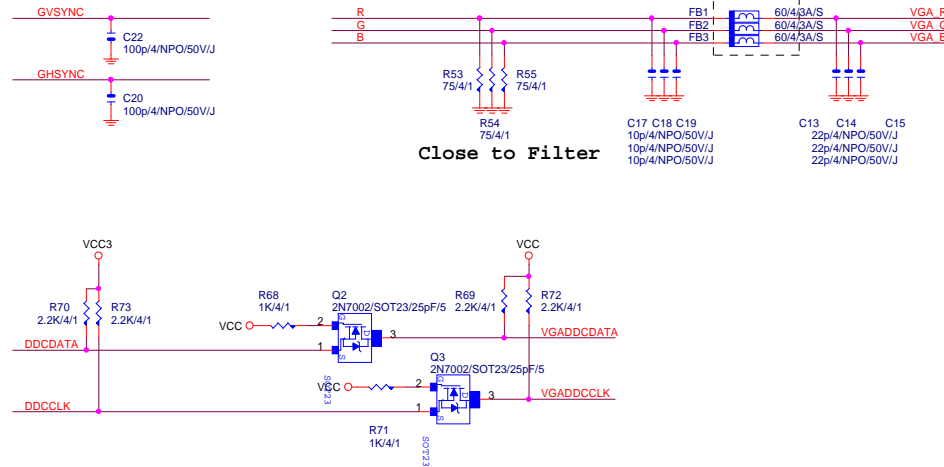
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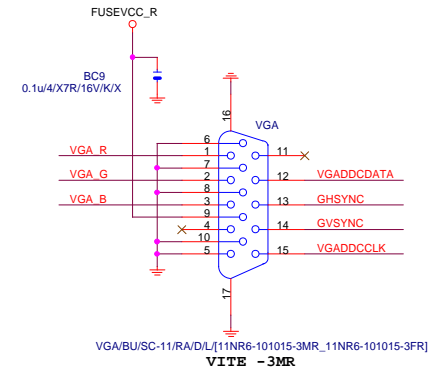
VGA ESD



VGA SIGNAL



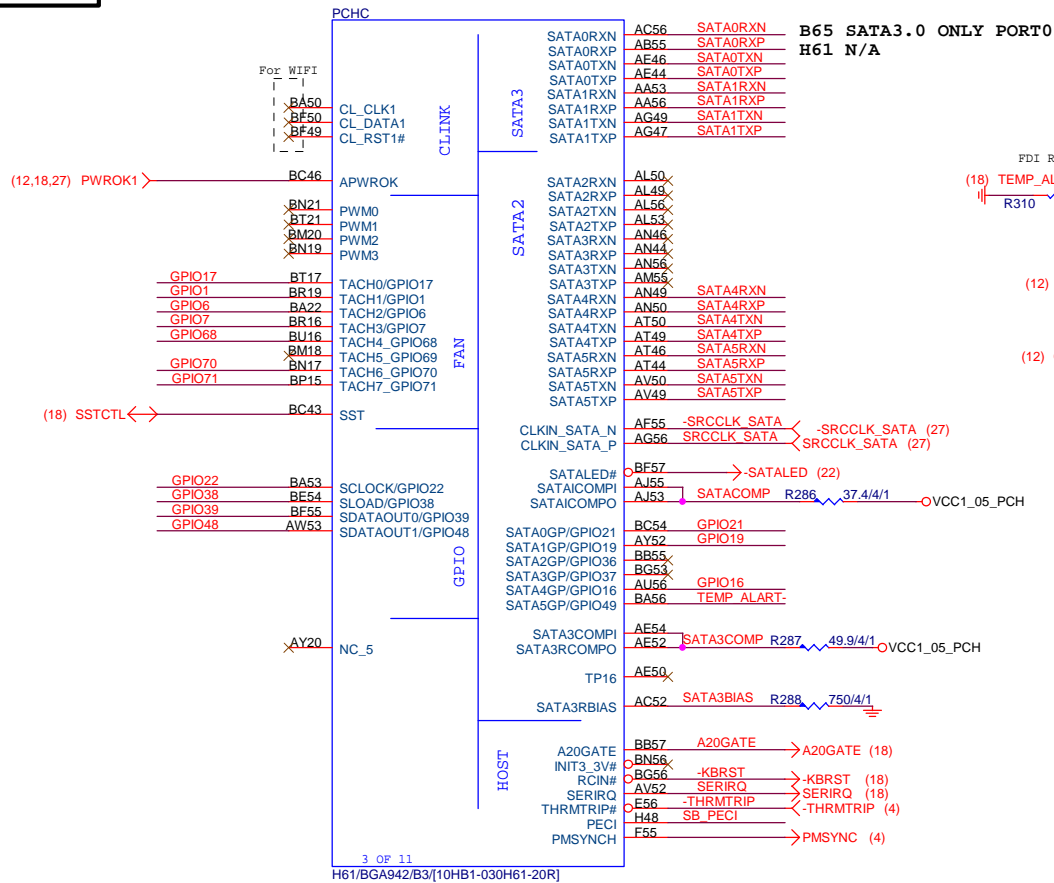
D-SUB



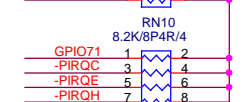
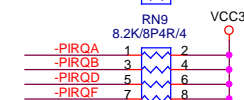
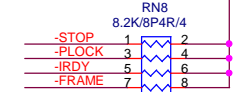
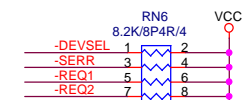
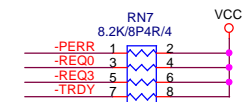
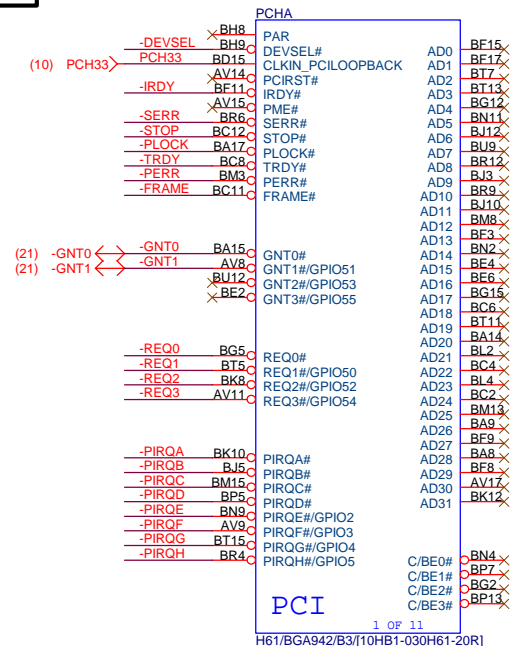
Gigabyte Technology

Title			PCH DISPLAY, CLK BUFFER		
Size			GA-H61M-S2P-GQ		
Date:			Friday, July 06, 2012		
Sheet			10 of 32		

PCH C



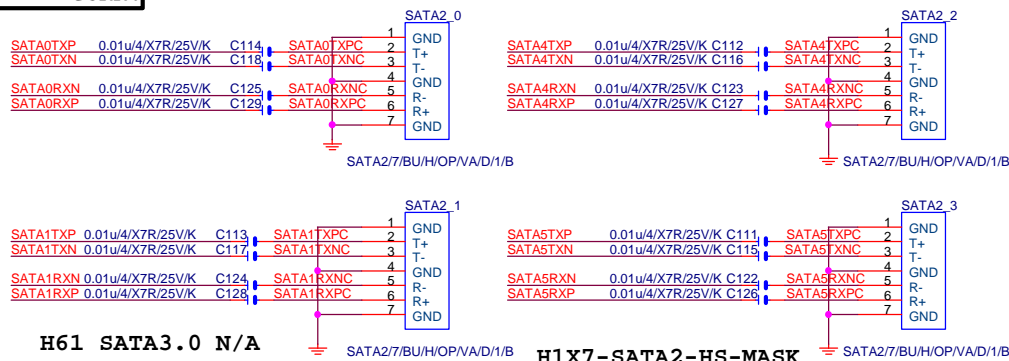
PCH A



Gigabyte Technology

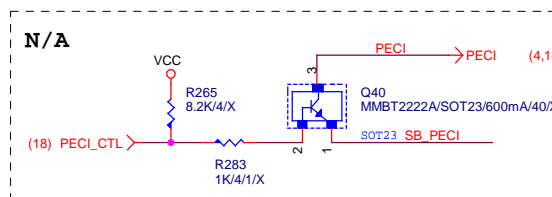
Title			
PCH HOST , SATA, PCI			
Size B	Document Number		Rev
	GA-H61M-S2P-GQ		1.0
Date:	Friday, July 06, 2012	Sheet	11 of 32

SATA CONN.

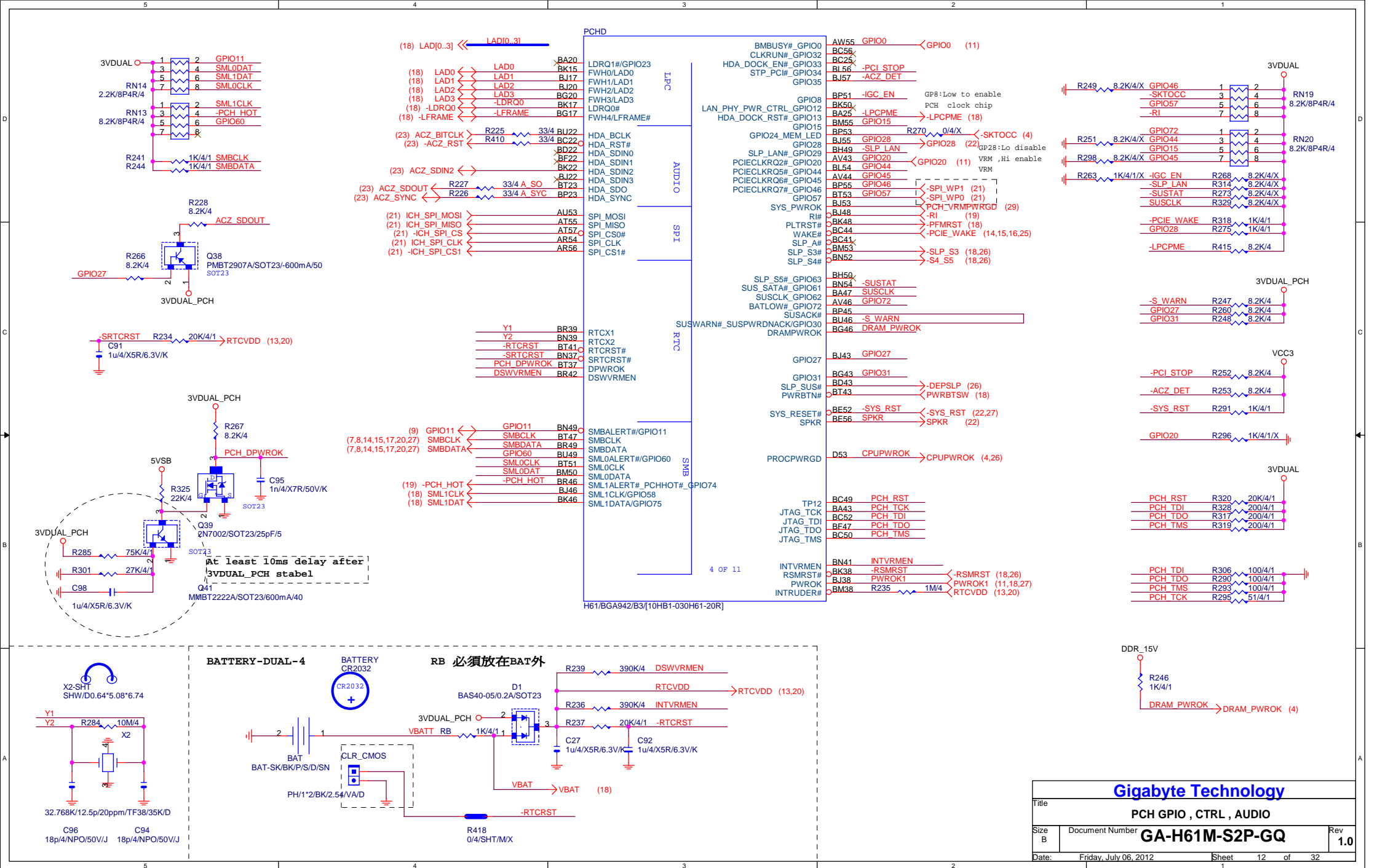


H61 SATA3.0 N/A

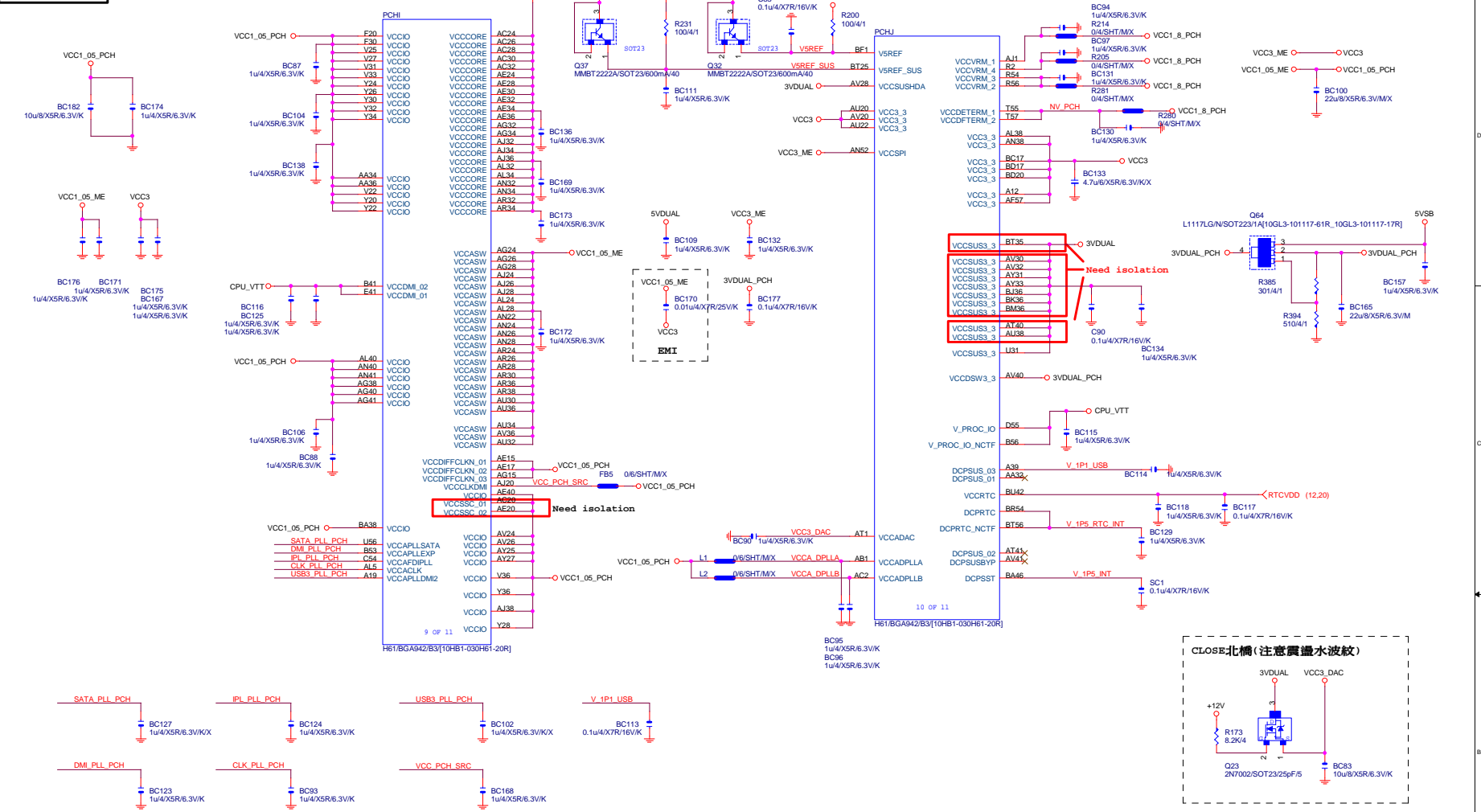
H1X7-SATA2-HS-MASK



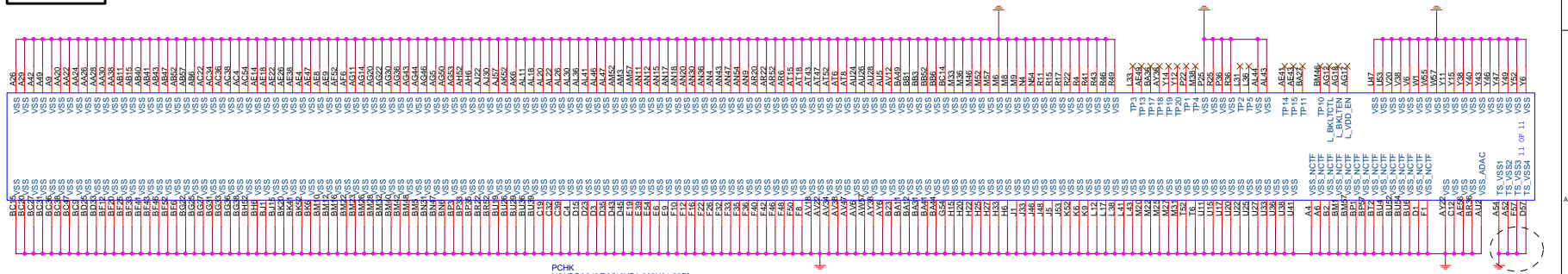
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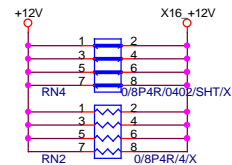
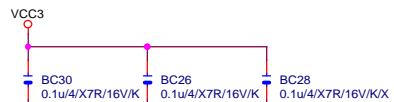
PCH I POWER



PCH K GND



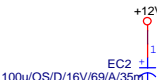
PCIEX16



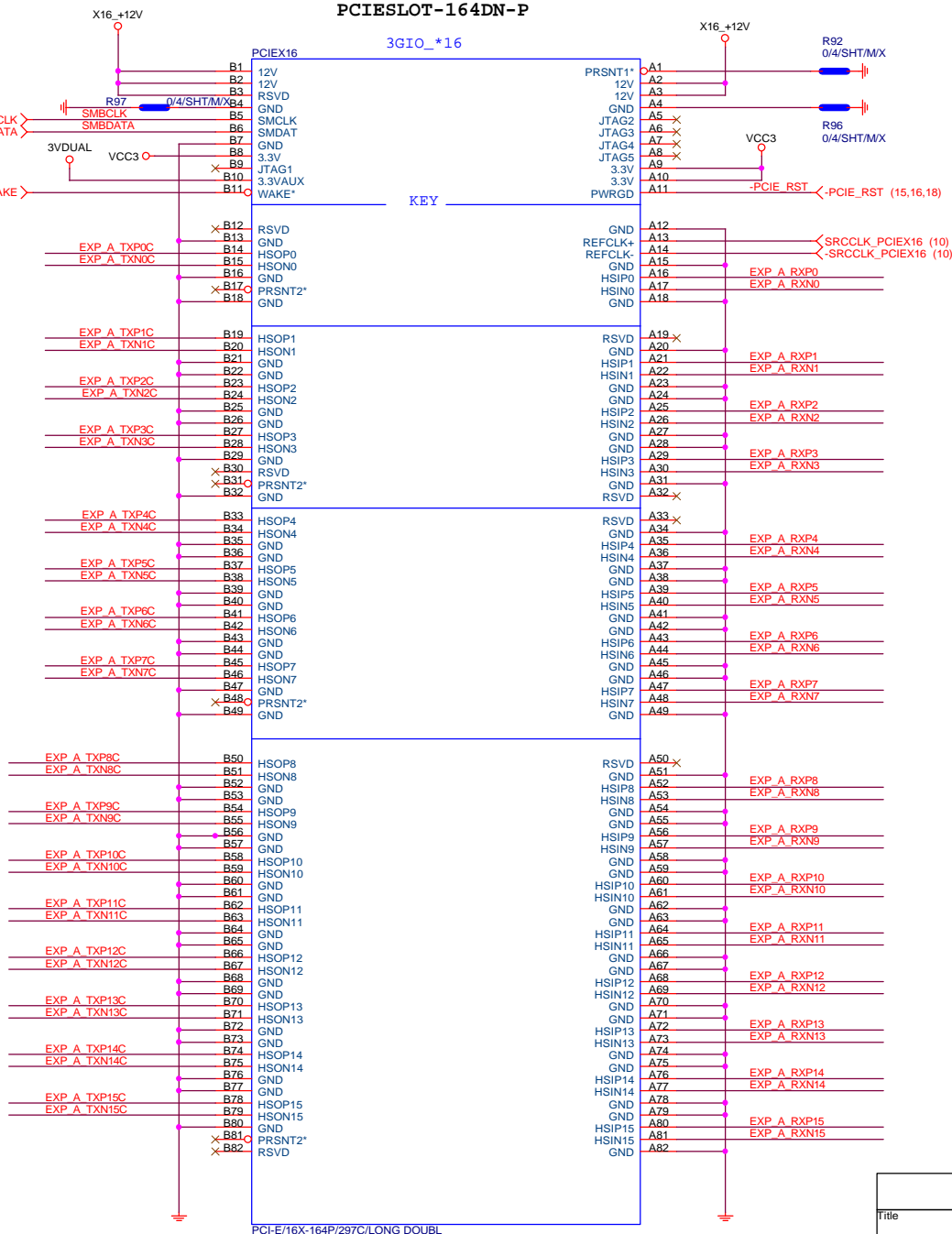
EXP A RXP0..15] >> EXP_A_RXP[0..15] (4)
 EXP A RXN0..15] >> EXP_A_RXN[0..15] (4)
 EXP A TXP0..15] >> EXP_A_TXP[0..15] (4)
 EXP A TXN0..15] >> EXP_A_TXN[0..15] (4)

EXP A TXP0	C32	0.22u/4/X5R/6.3V/K	EXP A TXP0C
EXP A TXN0	C30	0.22u/4/X5R/6.3V/K	EXP A TXN0C
EXP A TXP1	C35	0.22u/4/X5R/6.3V/K	EXP A TXP1C
EXP A TXN1	C37	0.22u/4/X5R/6.3V/K	EXP A TXN1C
EXP A TXP2	C39	0.22u/4/X5R/6.3V/K	EXP A TXP2C
EXP A TXN2	C41	0.22u/4/X5R/6.3V/K	EXP A TXN2C
EXP A TXP3	C43	0.22u/4/X5R/6.3V/K	EXP A TXP3C
EXP A TXN3	C45	0.22u/4/X5R/6.3V/K	EXP A TXN3C
EXP A TXP4	C46	0.22u/4/X5R/6.3V/K	EXP A TXP4C
EXP A TXN4	C49	0.22u/4/X5R/6.3V/K	EXP A TXN4C
EXP A TXP5	C50	0.22u/4/X5R/6.3V/K	EXP A TXP5C
EXP A TXN5	C51	0.22u/4/X5R/6.3V/K	EXP A TXN5C
EXP A TXP6	C52	0.22u/4/X5R/6.3V/K	EXP A TXP6C
EXP A TXN6	C54	0.22u/4/X5R/6.3V/K	EXP A TXN6C
EXP A TXP7	C57	0.22u/4/X5R/6.3V/K	EXP A TXP7C
EXP A TXN7	C58	0.22u/4/X5R/6.3V/K	EXP A TXN7C
EXP A TXP8	C60	0.22u/4/X5R/6.3V/K	EXP A TXP8C
EXP A TXN8	C61	0.22u/4/X5R/6.3V/K	EXP A TXN8C
EXP A TXP9	C62	0.22u/4/X5R/6.3V/K	EXP A TXP9C
EXP A TXN9	C63	0.22u/4/X5R/6.3V/K	EXP A TXN9C
EXP A TXP10	C64	0.22u/4/X5R/6.3V/K	EXP A TXP10C
EXP A TXN10	C65	0.22u/4/X5R/6.3V/K	EXP A TXN10C
EXP A TXP11	C66	0.22u/4/X5R/6.3V/K	EXP A TXP11C
EXP A TXN11	C67	0.22u/4/X5R/6.3V/K	EXP A TXN11C
EXP A TXP12	C68	0.22u/4/X5R/6.3V/K	EXP A TXP12C
EXP A TXN12	C70	0.22u/4/X5R/6.3V/K	EXP A TXN12C
EXP A TXP13	C72	0.22u/4/X5R/6.3V/K	EXP A TXP13C
EXP A TXN13	C73	0.22u/4/X5R/6.3V/K	EXP A TXN13C
EXP A TXP14	C74	0.22u/4/X5R/6.3V/K	EXP A TXP14C
EXP A TXN14	C75	0.22u/4/X5R/6.3V/K	EXP A TXN14C
EXP A TXP15	C77	0.22u/4/X5R/6.3V/K	EXP A TXP15C
EXP A TXN15	C78	0.22u/4/X5R/6.3V/K	EXP A TXN15C

(7,8,12,15,17,20,27) SMBCLK
 (7,8,12,15,17,20,27) SMBDATA
 (12,15,16,25) -PCIE_WAKE



PCIESLOT-164DN-P

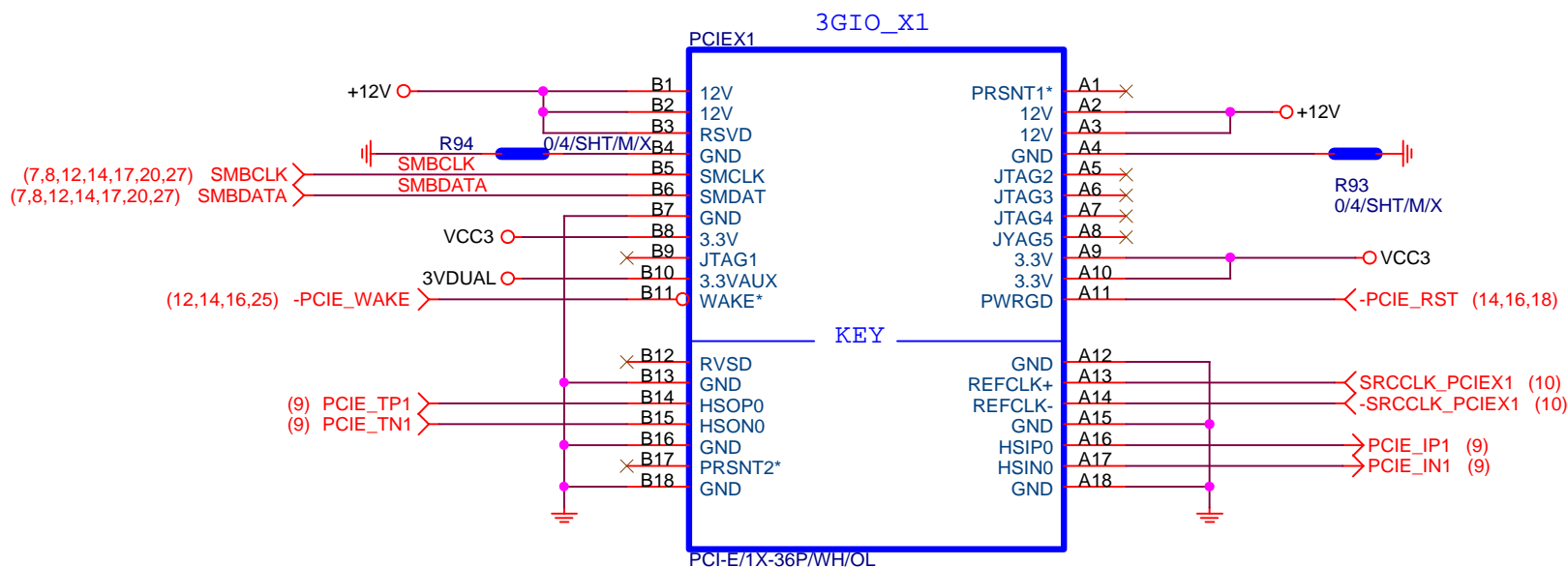


PCI-E/16X-164P/297C/LONG DOUBL

DOUBLE PUSH LATCH

Gigabyte Technology

Title			
PCI EXPRESS * 16			
Size			
Custom			
Document Number			
GA-H61M-S2P-GQ			
Date:			
Friday, July 06, 2012			
Sheet			
14 of 32			
Rev			
1.0			



3VDUAL

BC31
0.1u/4/X7R/16V/K/X

+12V

BC23
0.1u/4/X7R/16V/K/X

VCC3

BC25
0.1u/4/X7R/16V/K/X

-PCIE_RST

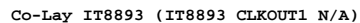
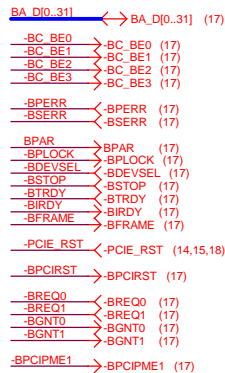
C28
10p/4/NPO/50V/J

Gigabyte Technology

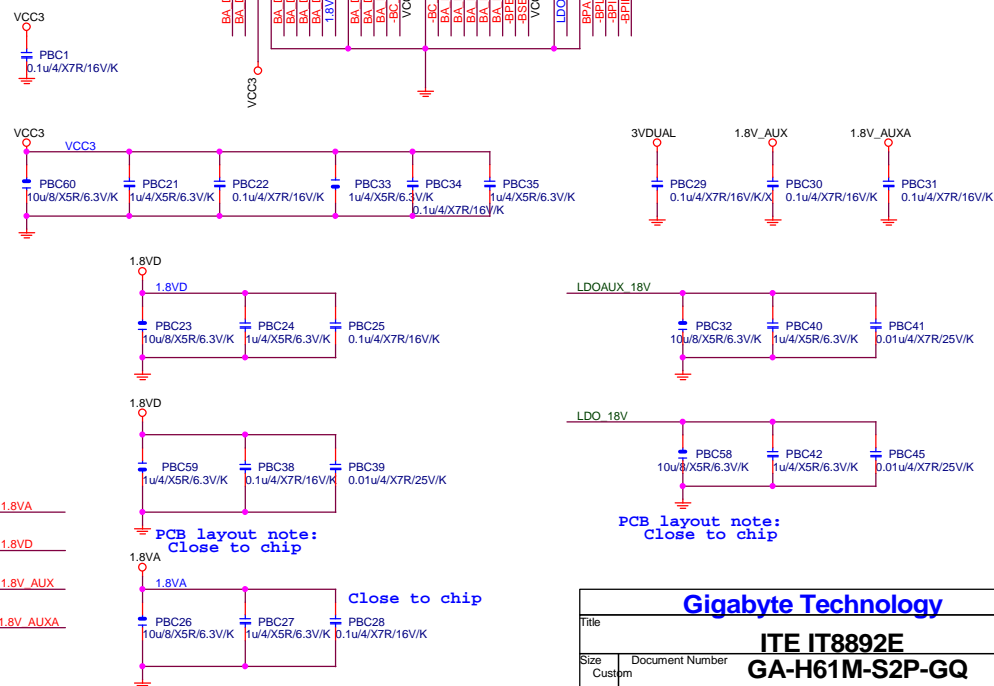
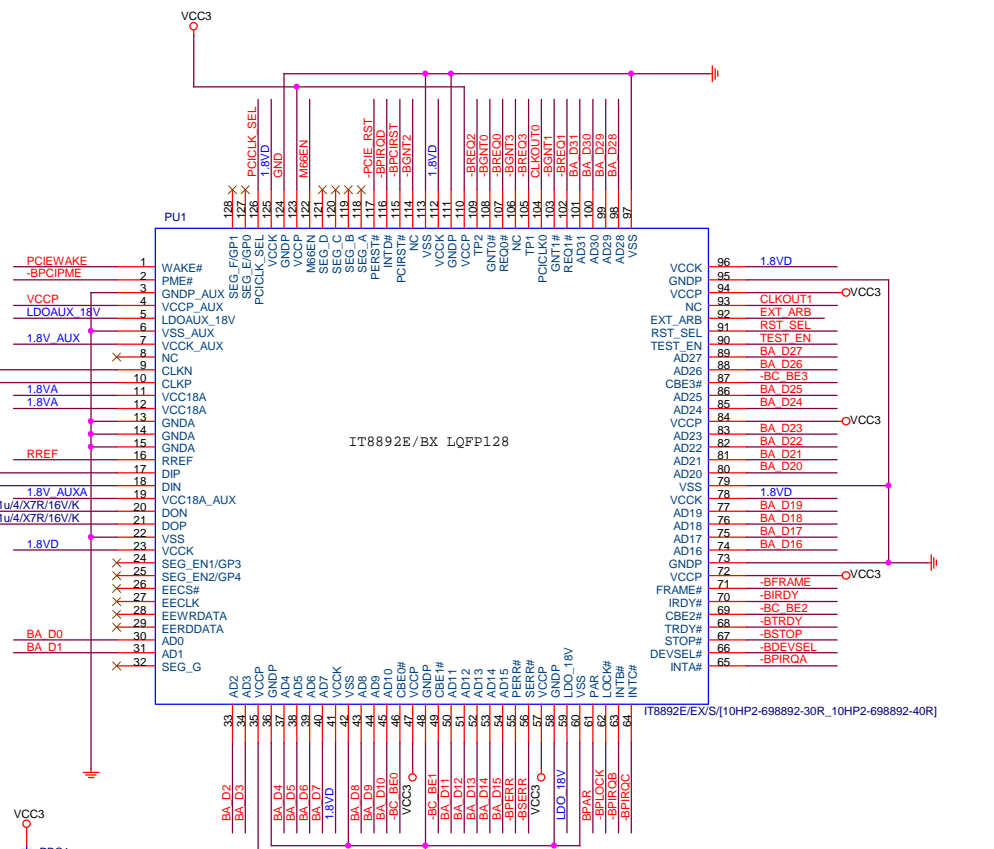
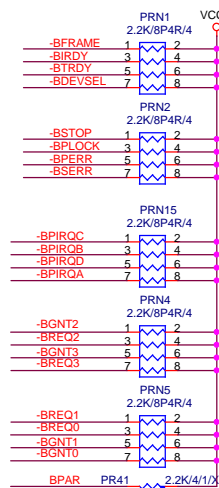
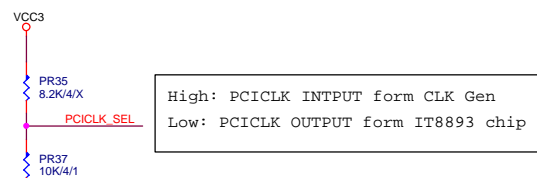
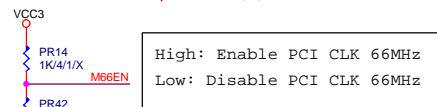
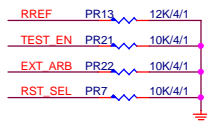
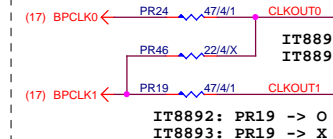
Title			
PCI EXPRESS X 1 PORT			
Size A	Document Number		Rev
	GA-H61M-S2P-GQ		1.0
Date:	Friday, July 06, 2012	Sheet	15 of 32

PCIE TO PCI

PCI:5/4/5 Impedance=50 +- 15%



```
IT8892: PR24 -> 47ohm
IT8893: PR24 -> 22ohm
```

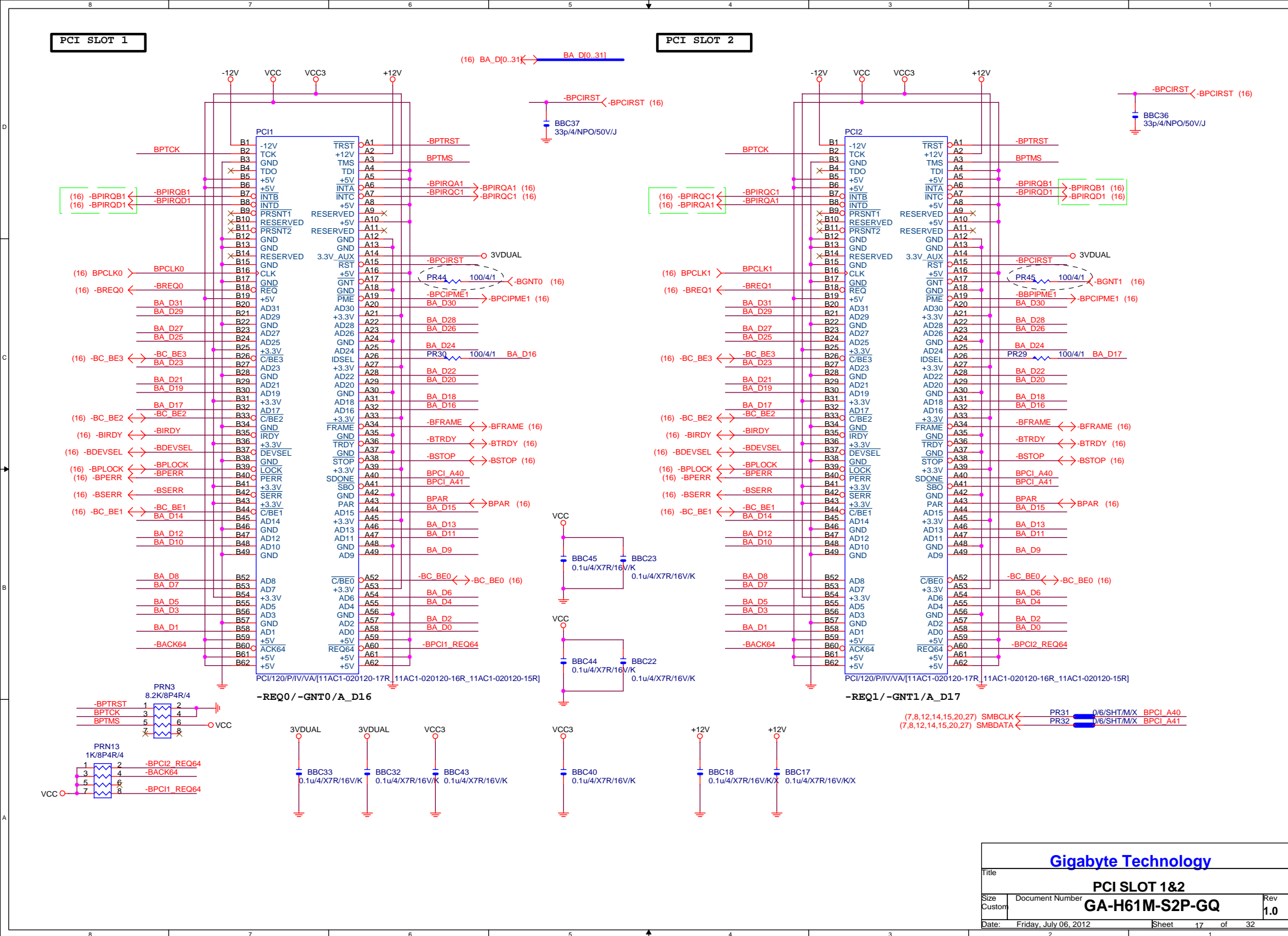


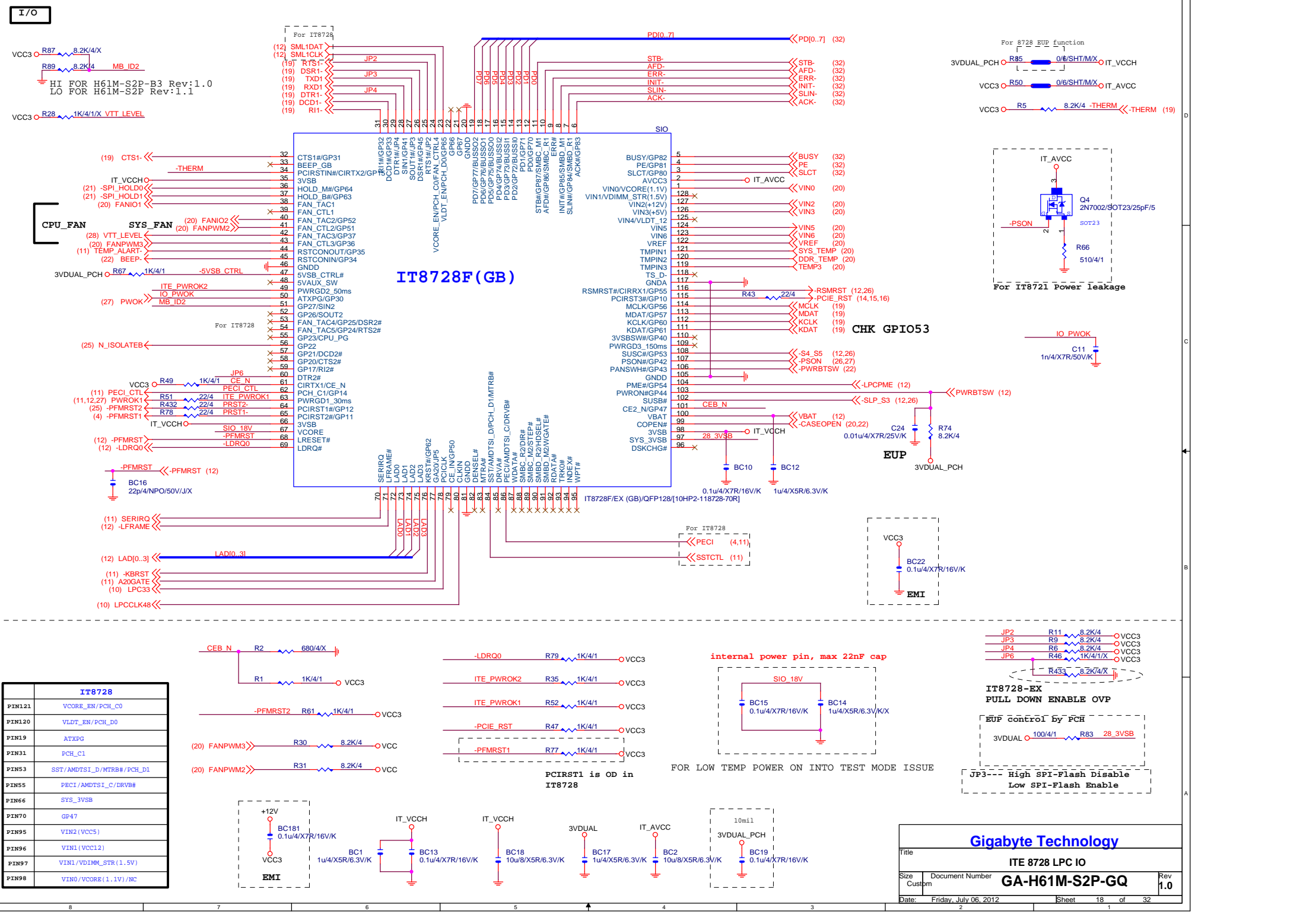
Gigabyte Technology

ITE IT8892E

GA-H61M-S2P-GQ

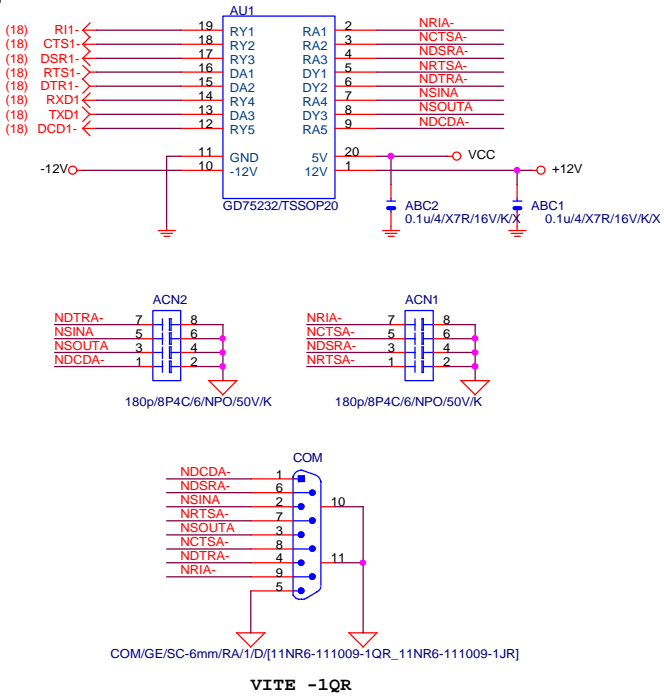
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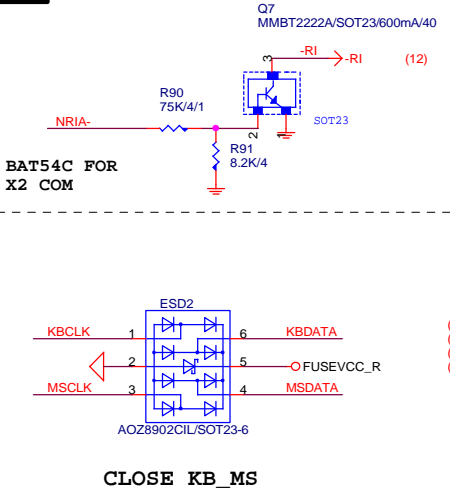


	IT8728
PIN121	VCORE_EN/PCH_C0
PIN120	VLDT_EN/PCH_D0
PIN19	ATXPG
PIN31	PCH_C1
PIN53	SST/AMDTSI_D/MTRB#/PCH_D1
PIN55	PECI/AMDTSI_C/DRV#
PIN66	SYS_3VSB
PIN70	GP47
PIN95	VIN2 (VCC5)
PIN96	VIN1 (VCC12)
PIN97	VIN1/VDIMM_STR (1.5V)
PIN98	VIN0/VCORE (1.1V)/NC

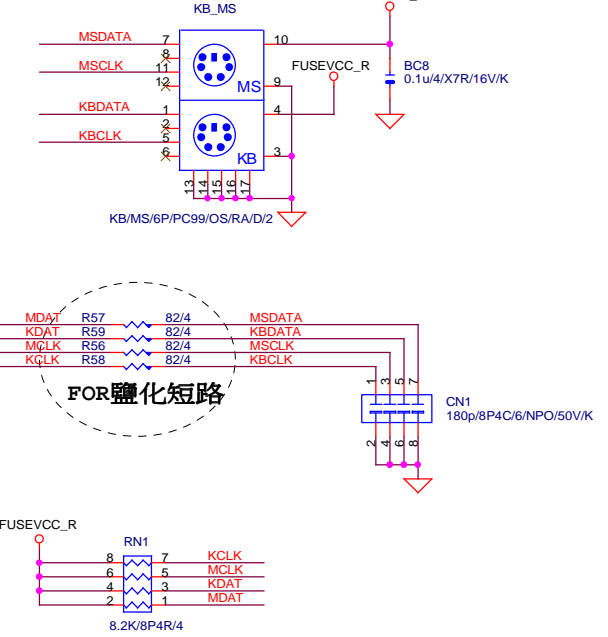
COM



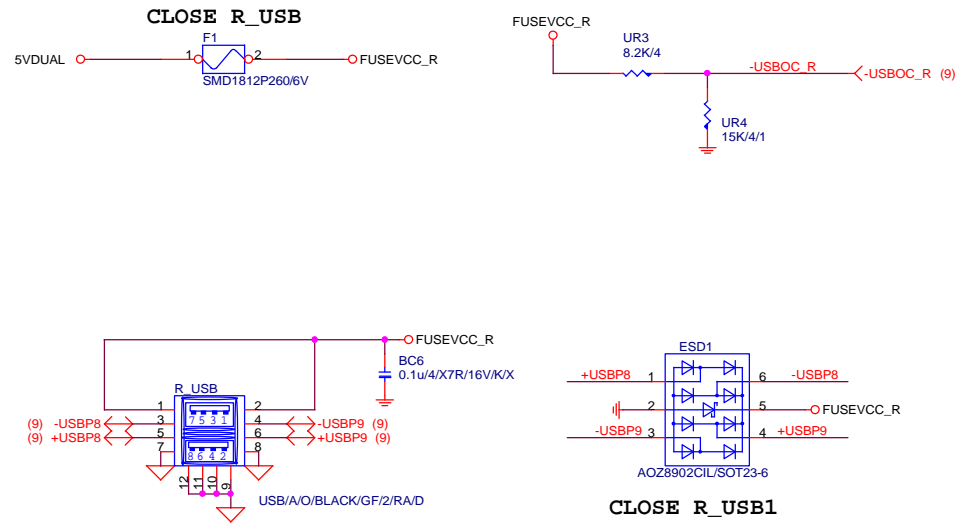
COM RI



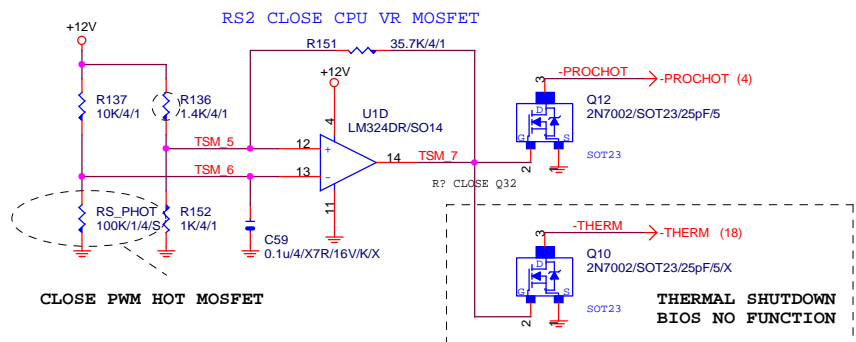
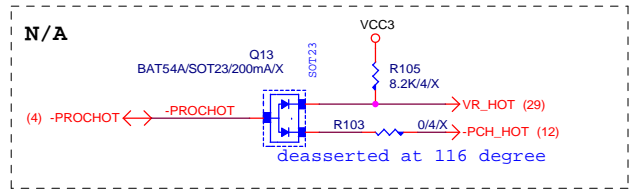
KB/MS



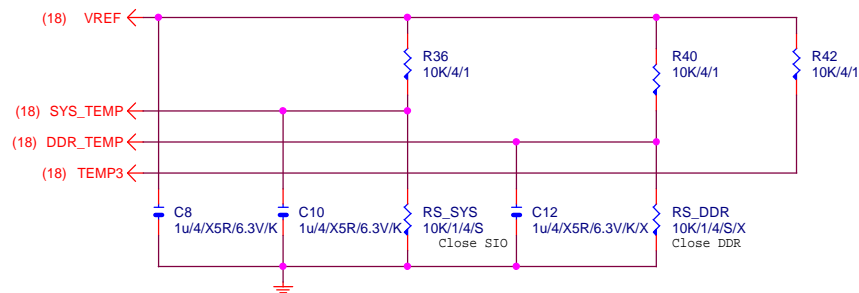
R_USB1



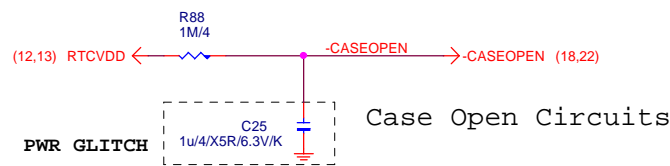
-PROHOT



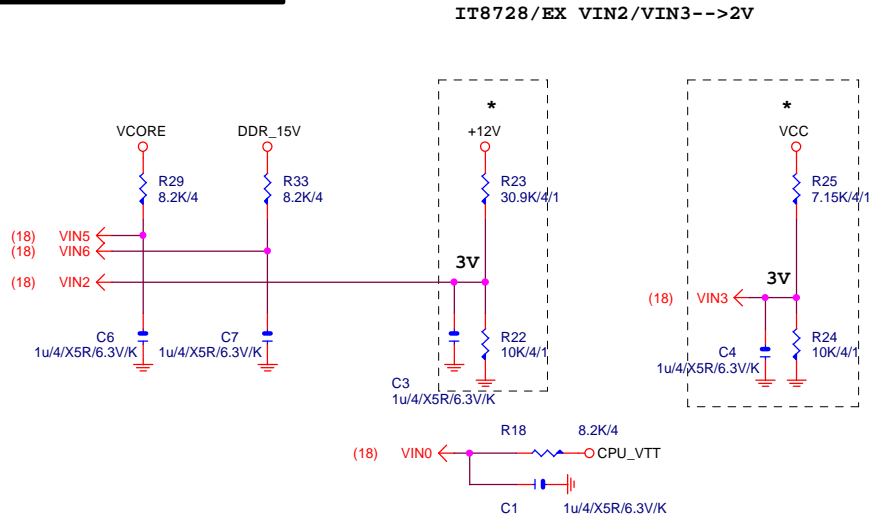
TEMP H/W MONITOR



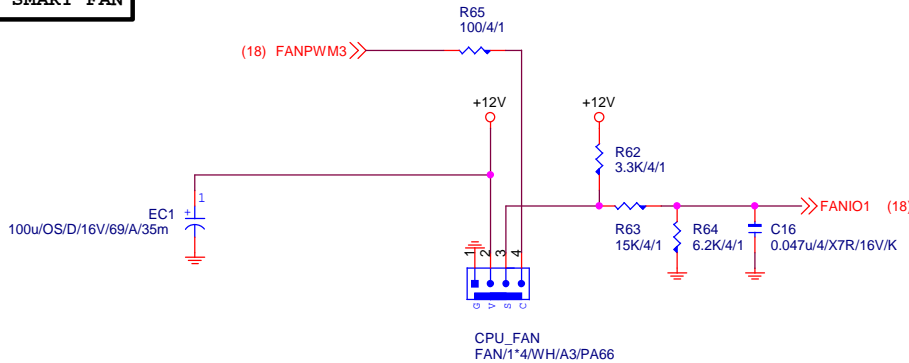
CASE OPEN



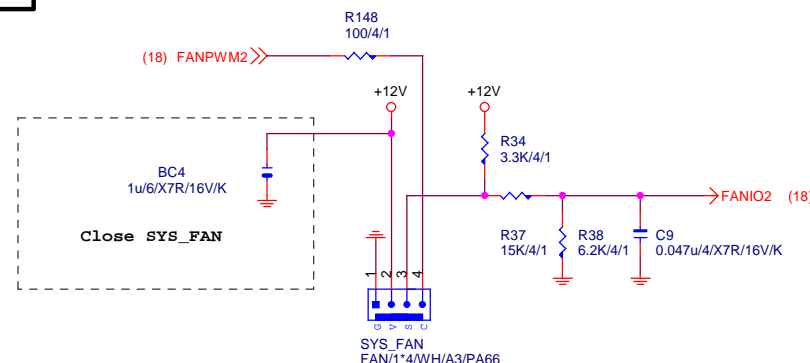
VOLTAGE-- H/W MONITOR



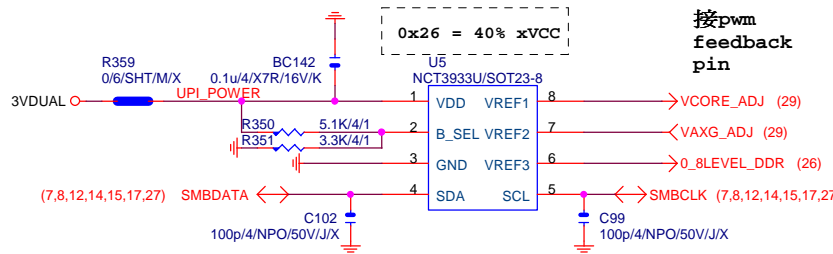
CPU SMART FAN



SYS SMART FAN



O.V.



DUAL BIOS

VCC3

R377
0/4/SHT/M/X

BC163
0.1u/4/X7R/16V/K

M_BIOS

-ICH_SPI_CS

SPI_MISO

-SPI_WP0

C106
10p/4/NPO/50V/J/X

CS#

SO

WP#

VSS

VDD

HOLD#

SCK

SI

32M/SPI/SO8/200mil/S

MAIN BIOS

VCC3

R366
0/4/SHT/M/X

BC145
0.1u/4/X7R/16V/K

B_BIOS

-ICH_SPI_CS

SPI_MISO

-SPI_WP1

CS#

SO

WP#

VSS

VDD

HOLD#

SCK

SI

32M/SPI/SO8/200mil/S

BACKUP BIOS

(12) ICH_SPI_MOSI

(12) -ICH_SPI_CS

ICH_SPI_MOSI R337 8.2K/4/X

-ICH_SPI_CS R356 8.2K/4/X

SPI_HOLD0 R378 1K/4/1

-SPI_HOLD1 R347 1K/4/1

FOR BIOS TOOL

(12) -SPI_WP0

(12) ICH_SPI_MISO

(12) -ICH_SPI_CS1

(12) -SPI_WP1

-SPI_WP0 R364 8.2K/4/X

ICH_SPI_MISO R339 8.2K/4

-ICH_SPI_CS1 R258 8.2K/4/X

-SPI_WP1 R338 8.2K/4/X

(11) -GNT0

(11) -GNT1

R406 1K/4/1/X

R207 1K/4/1/X

FOR BIOS TOOL

SPI_MISO R345 22/4

<< ICH_SPI_MISO (12)

B65使用64M BIOS

使用H67暫用32M

H61使用32M BIOS

BOOT DEVICE	GNT1	GNT0
LPC	0	0
PCI	0	1
SPI	1	1

1 means floating
0 means PD 1K

Gigabyte Technology

TitleDUAL BIOS

Size ADocument NumberGA-H61M-S2P-GQRev1.0

Date: Friday, July 06, 2012Sheet 21 of 32

H61使用32M BIOS

BOOT DEVICE	GNT1	GNT0
LPC	0	0
PCI	0	1
SPI	1	1

```

1 means floating
0 means PD 1K

```

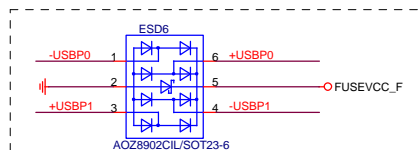
Gigabyte Technology

DUAL BIOS

Rev	
1.0	

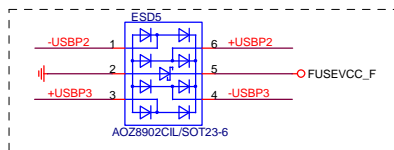
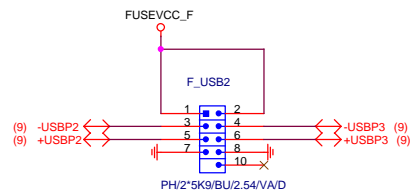
Sheet	21	of	32
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FRONT USB1

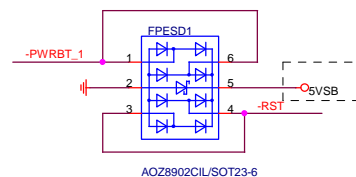


Close to connector

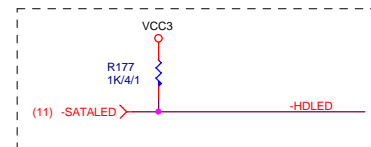
FRONT USB2



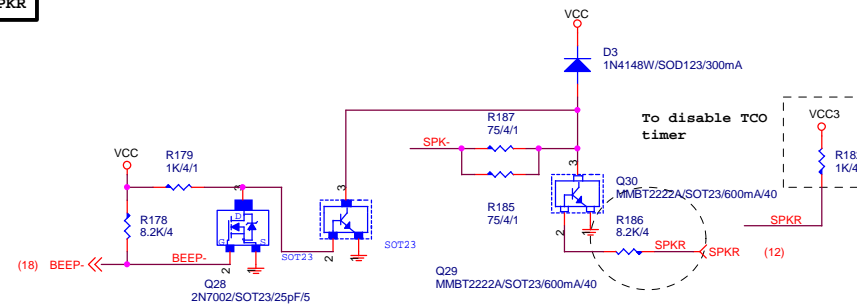
Close to connector



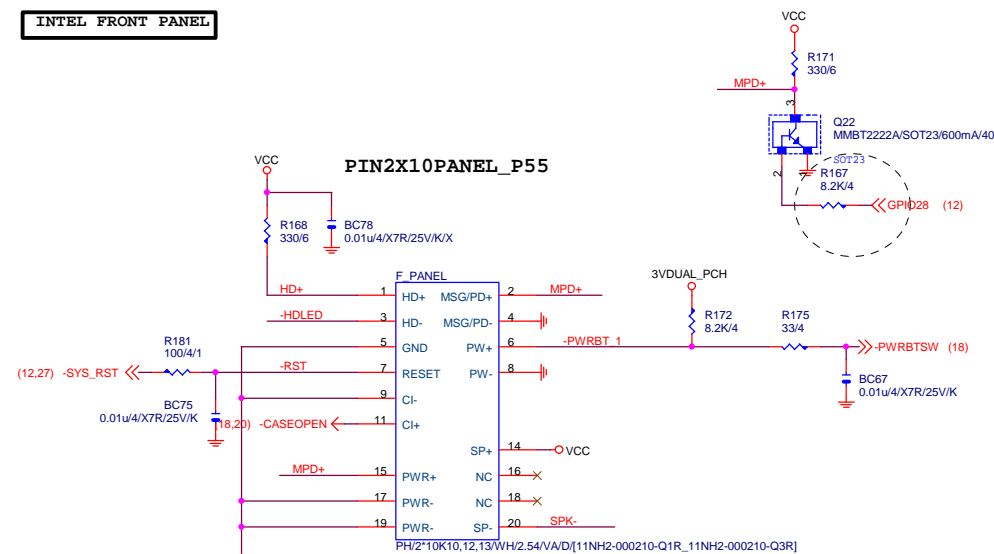
SATA LED



SPKR

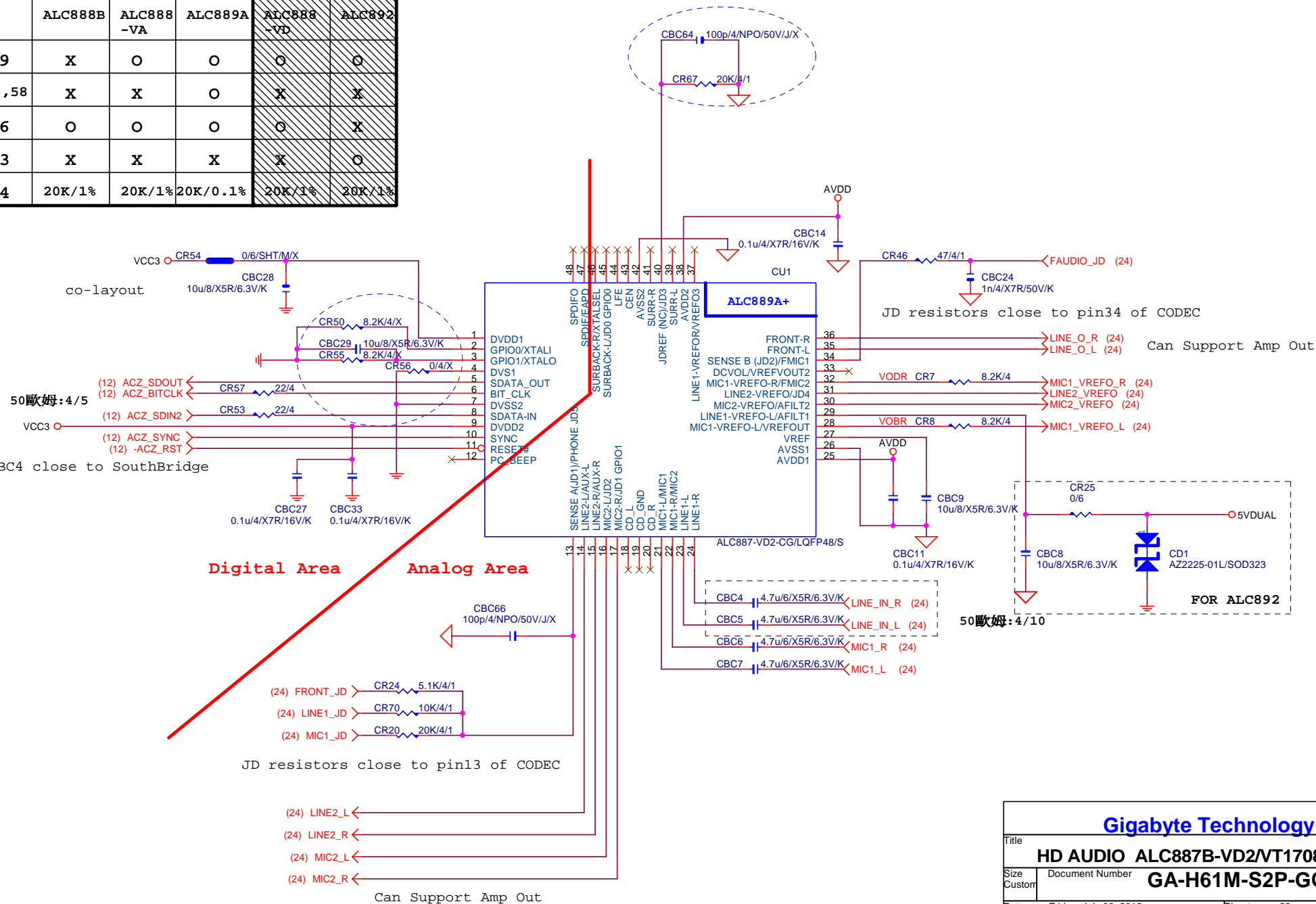


INTEL FRONT PANEL

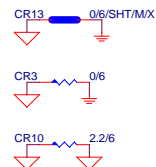
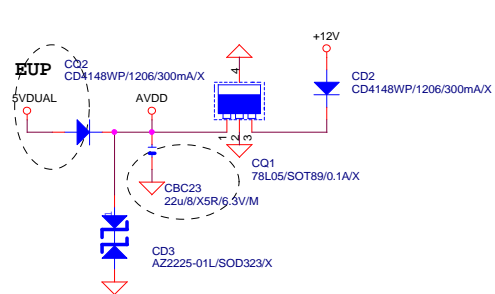


ALC892/ALC889A/ALC889/ALC888B Colay

	ALC888B	ALC888-VA	ALC889A	ALC888-VD	ALC892
CR59	X	O	O	O	O
CR53,58	X	X	O	X	X
CR56	O	O	O	O	X
CR63	X	X	X	X	O
CR34	20K/1%	20K/1%	20K/0.1%	20K/1%	20K/1%



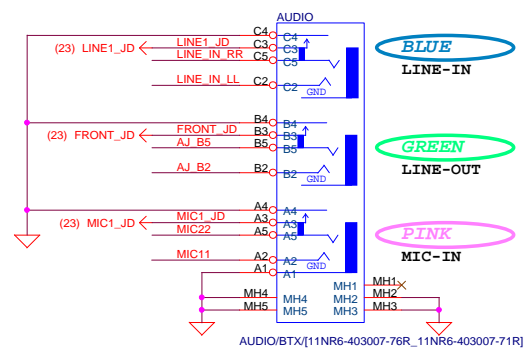
CODEC POWER/EMI PAD



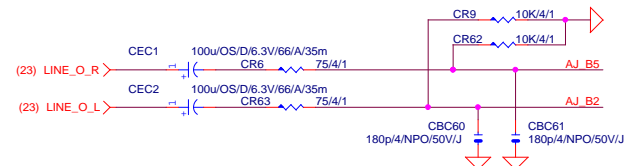
SPDIF

N/A

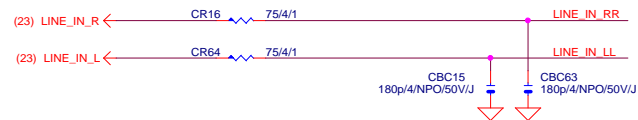
AZALIA JACK



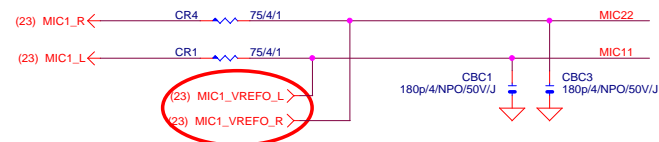
LINE-OUT



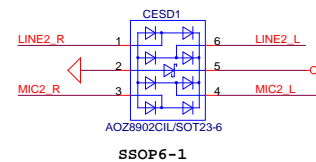
LINE-IN



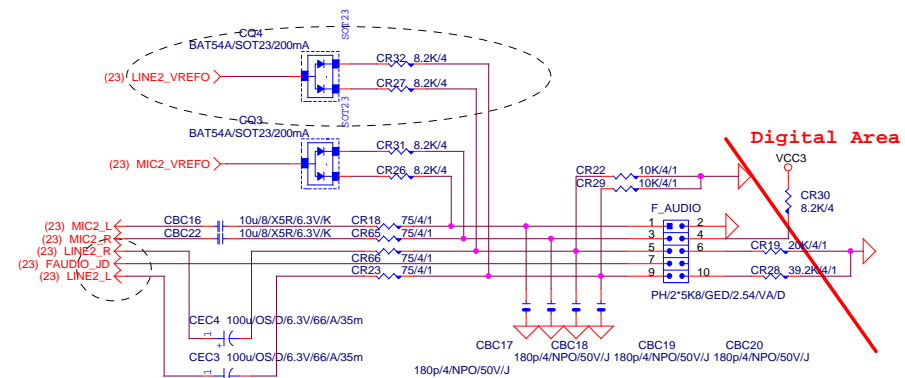
MIC-IN



F_AUDIOESD



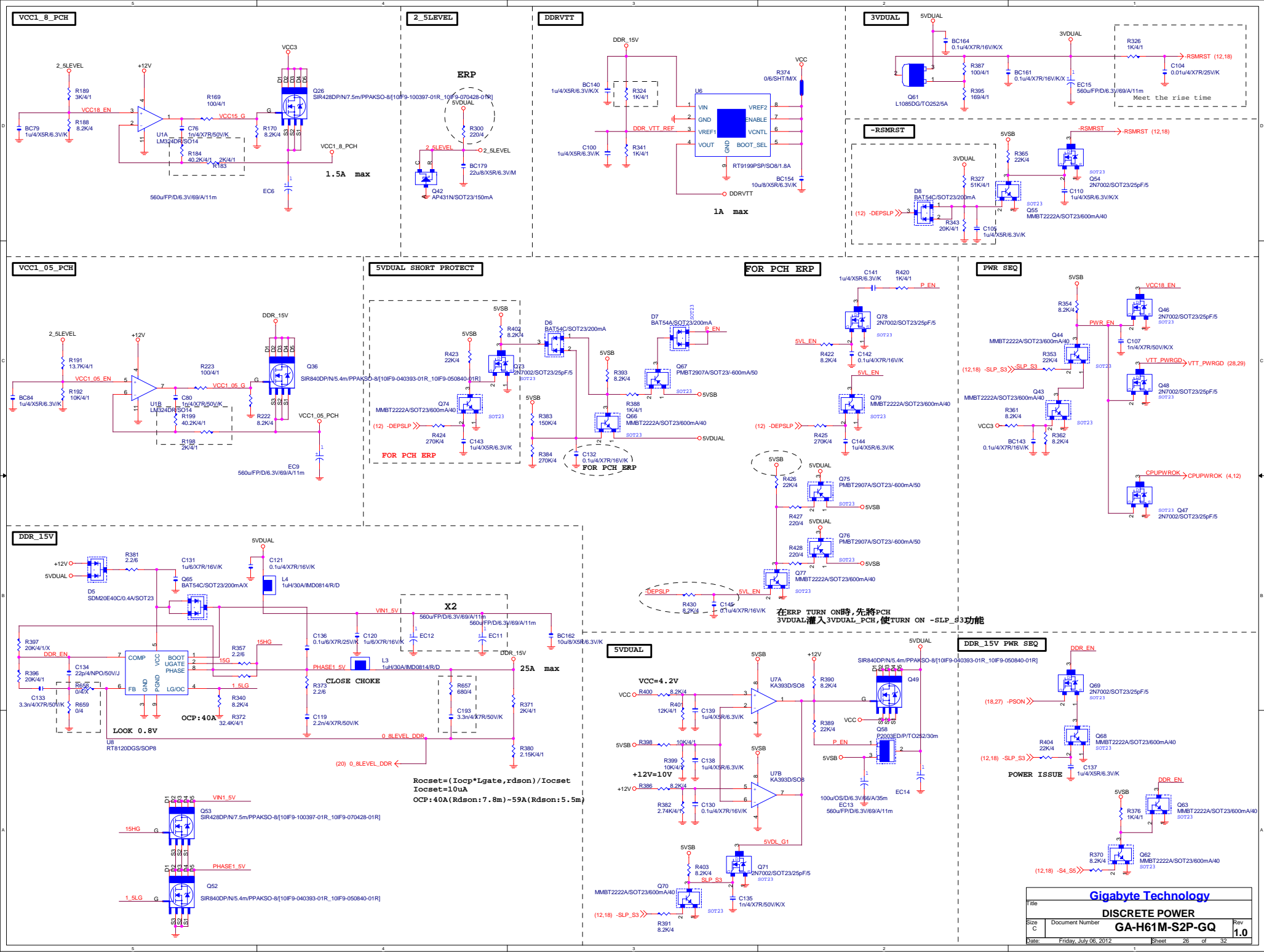
AZALIA FRONT PANEL



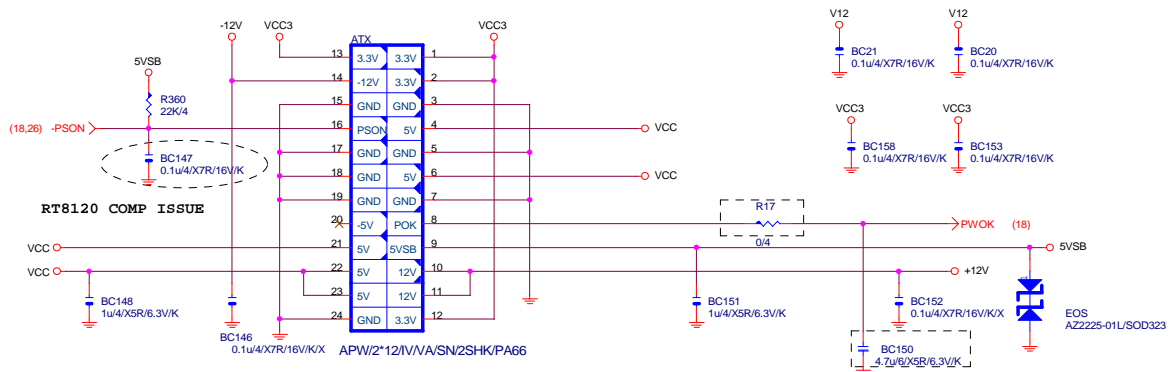
Gigabyte Technology

AUDIO JACK

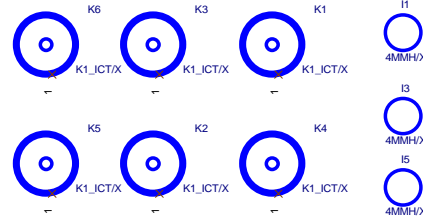
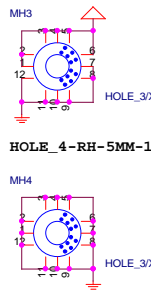
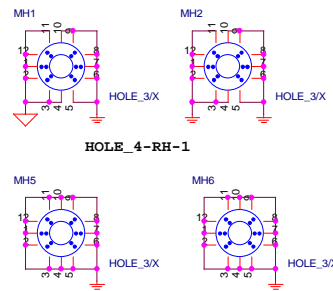
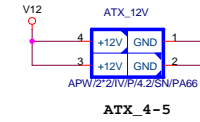
Title	Document Number	Rev
Size	GA-H61M-S2P-GQ	1.0
Custom		
Date:	Friday, July 06, 2012	Sheet 24 of 32



ATXX24 POWER CONNECTOR



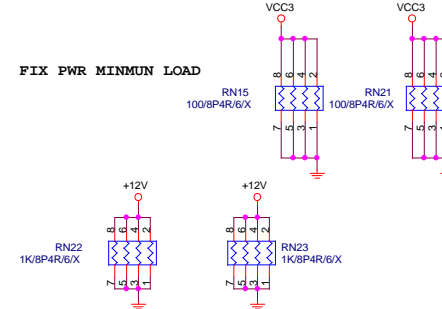
ATXX4 POWER CONNECTOR



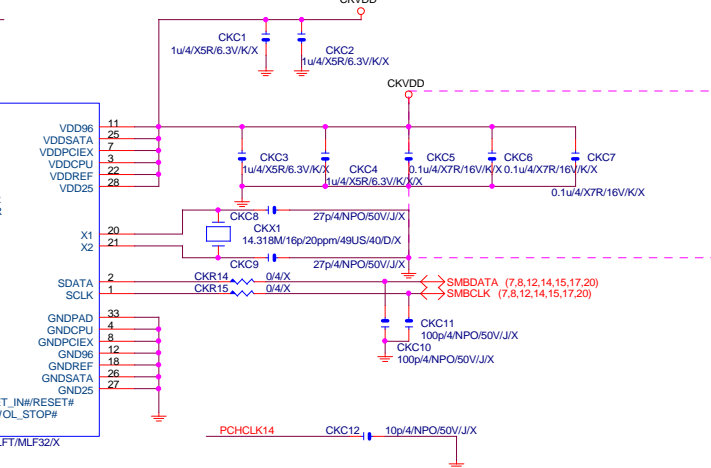
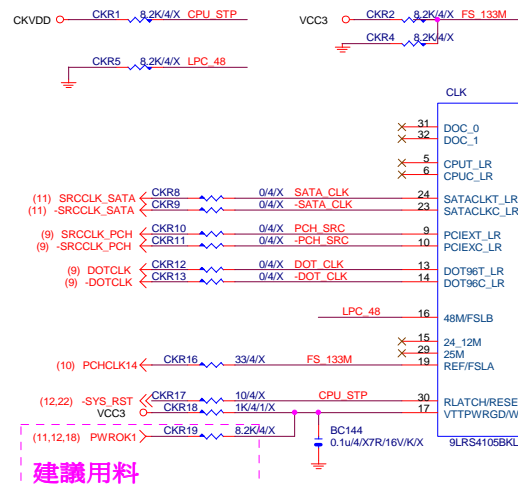
To prevent the 5VSB under loading when boot

MIN. LOAD

FIX PWR MINMUN LOAD



CLK GEN



CPU Frequency Selection	
FS	CPU
1	100M <Default>
0	133M

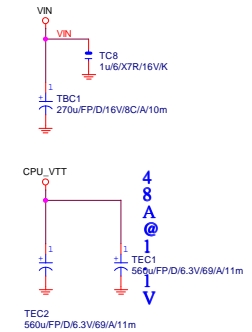
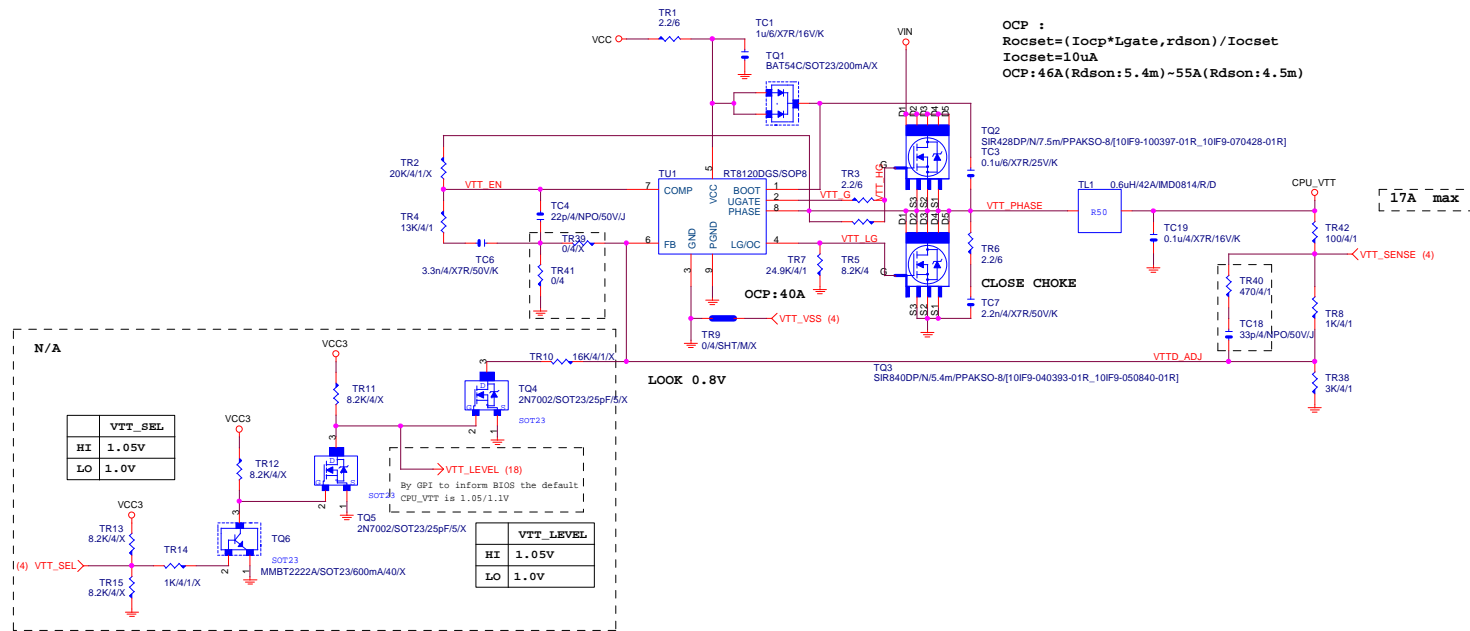
Gigabyte Technology

ATX CONNECTOR

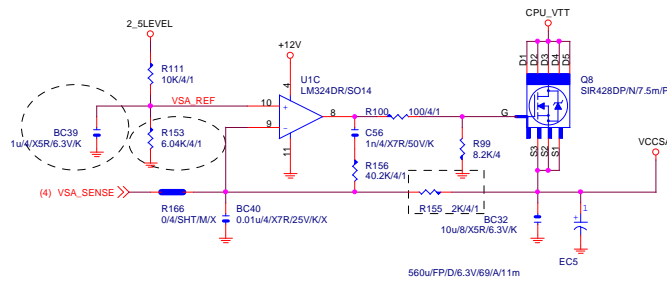
GA-H61M-S2P-GQ

Title		Rev 1.0
Size	Document Number	
Date:	Friday, July 06, 2012	Sheet 27 of 32

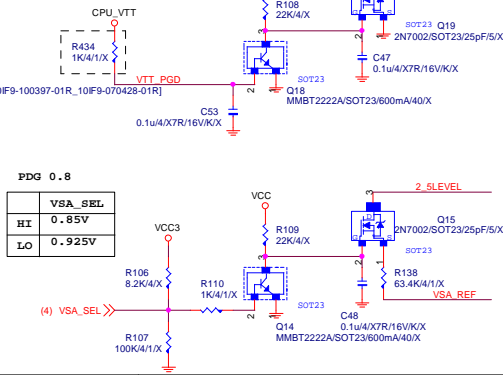
CPU_VTT



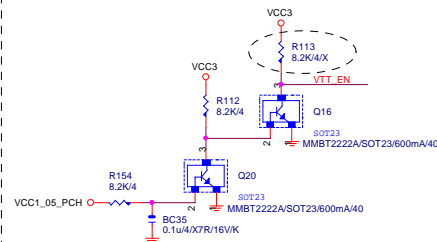
VCCSA



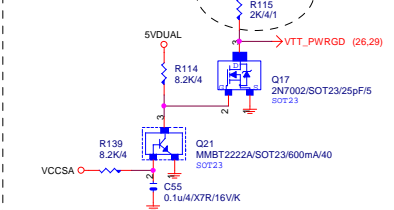
VCCSA_PWR_SEQ



CPU_VTT_PWR_SEQ

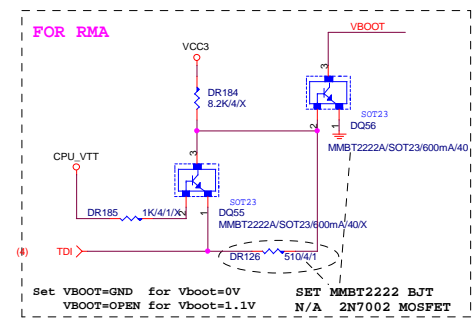
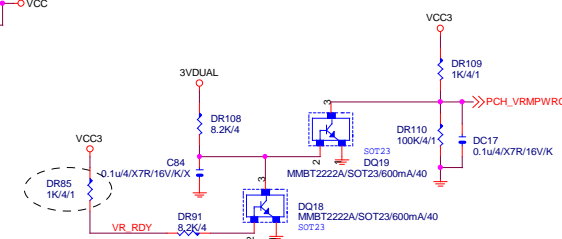
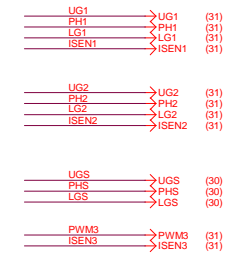
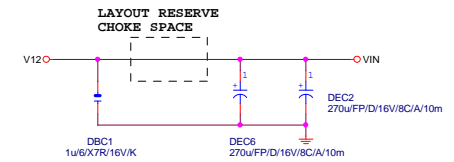
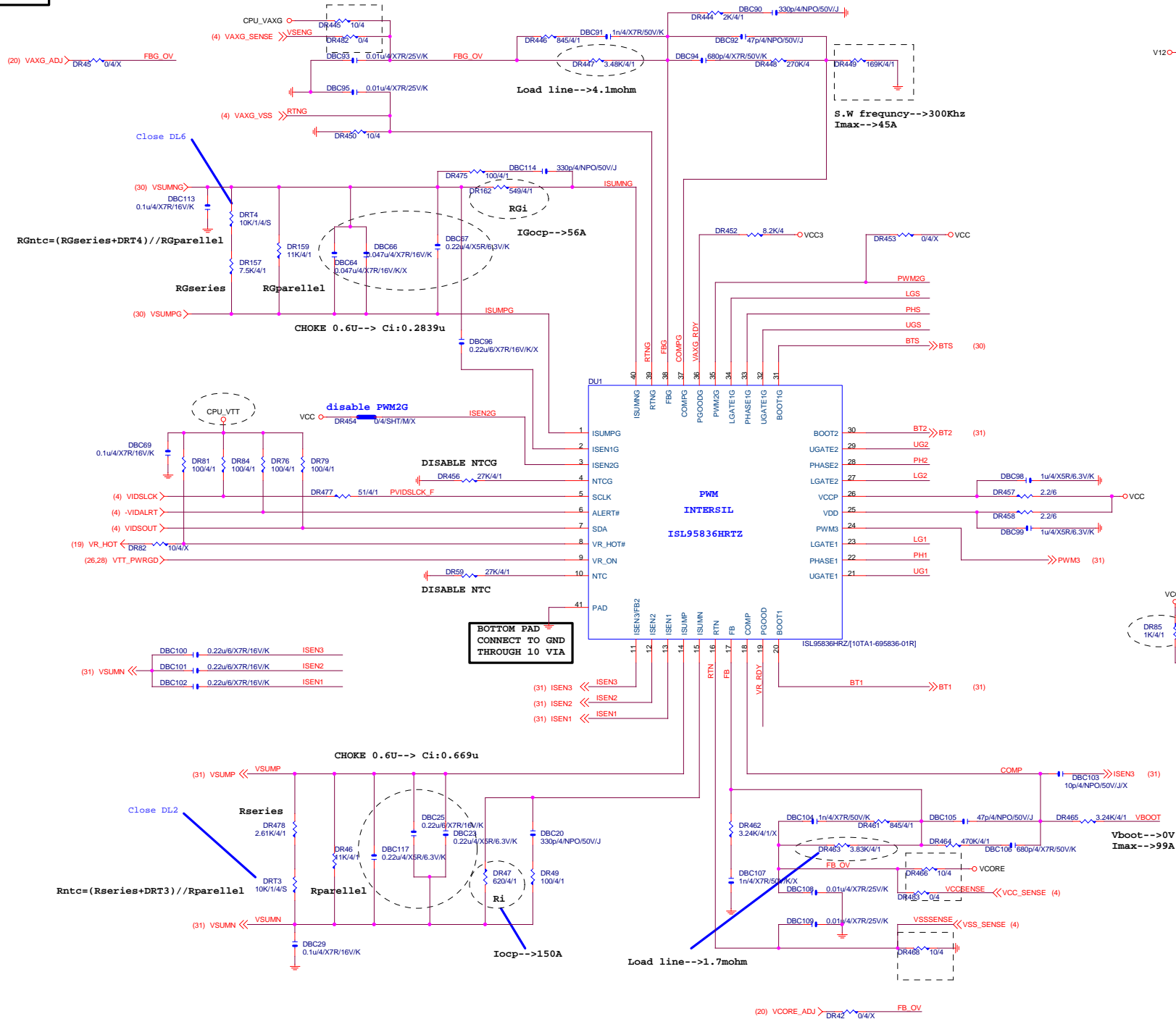


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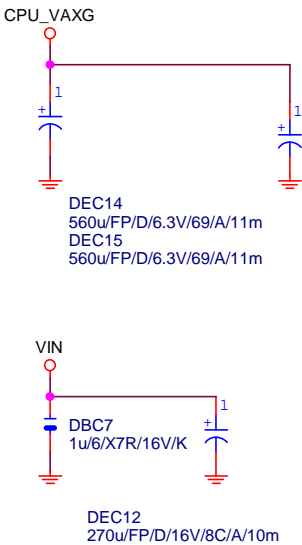
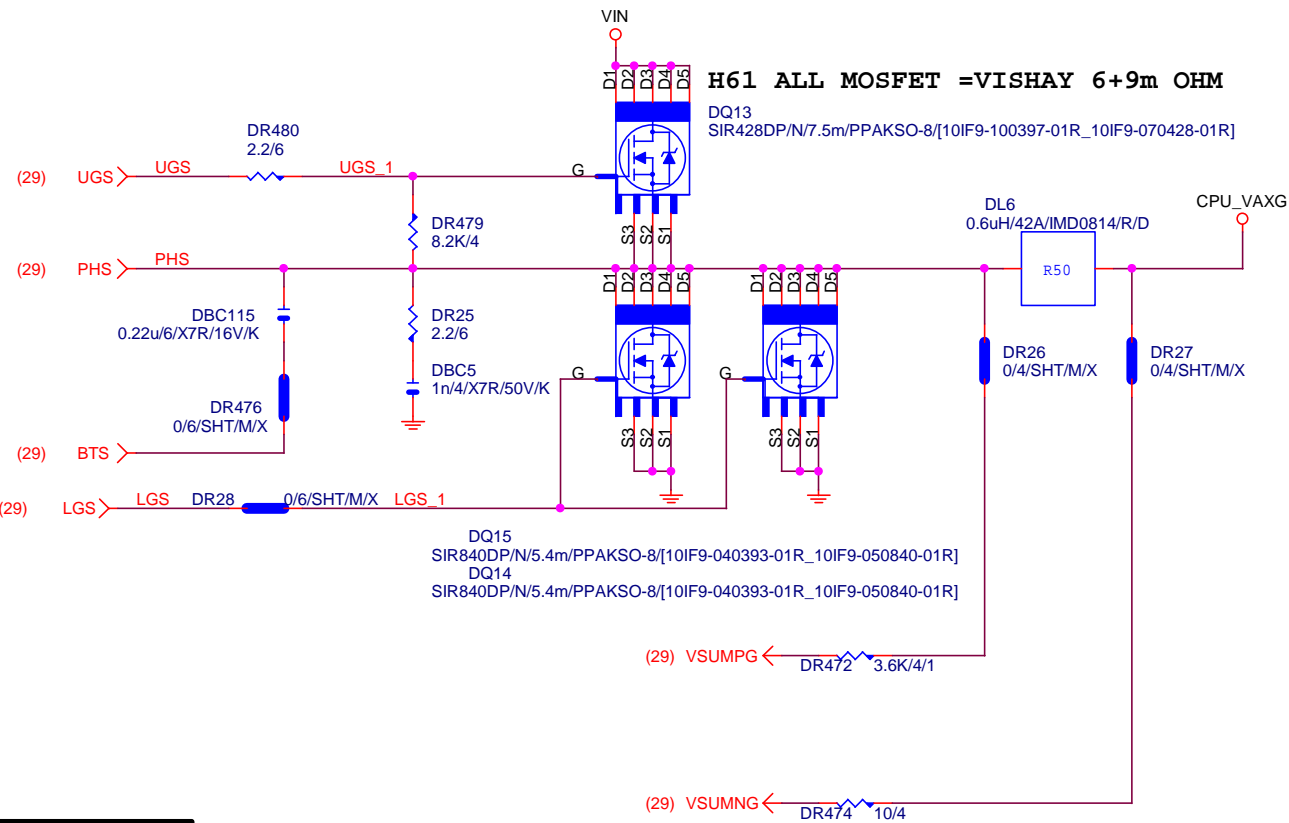


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CPU_VTT PWM_RT8120			
Size	Document Number	Rev	
Cuspm	GA-H61M-S2P-GQ	1.0	
Date	Friday, July 06, 2012	Sheet	28 of 32

VCORE PWM

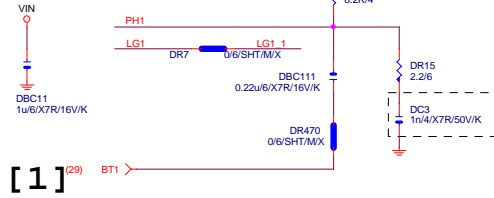


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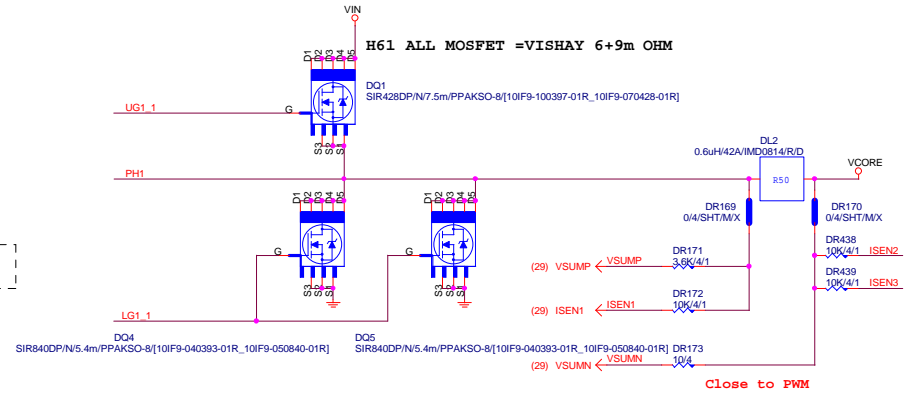


MOS HEATSINK

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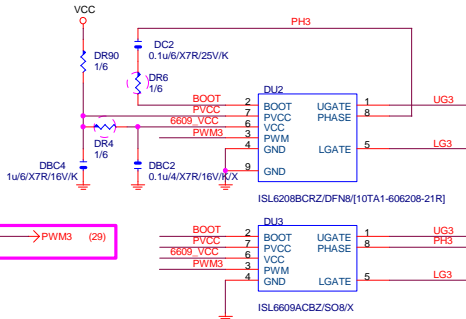


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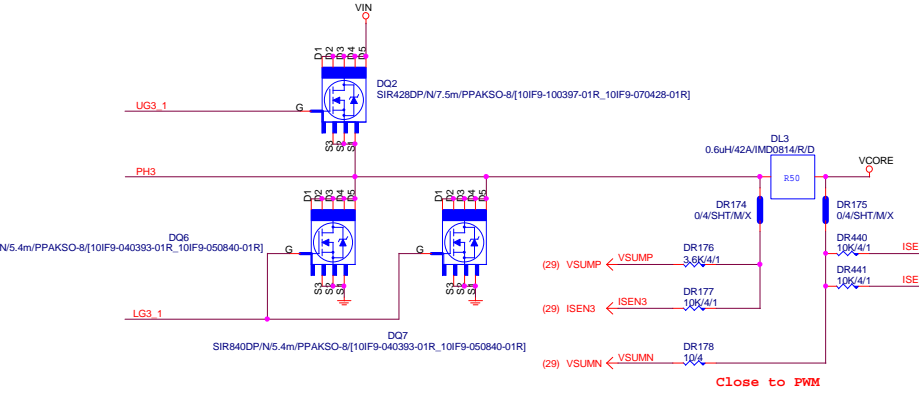


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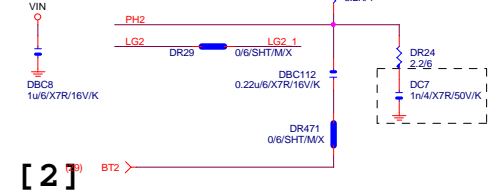
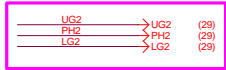
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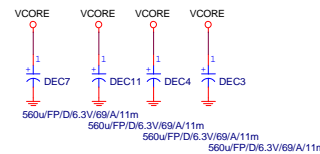
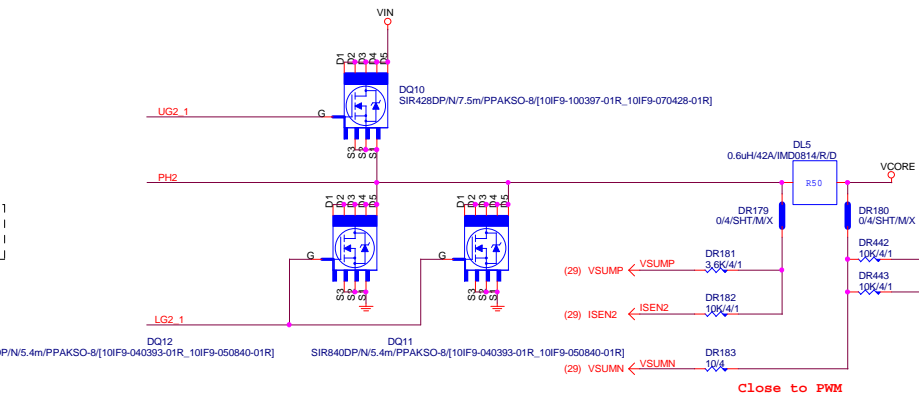
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PHASE 2



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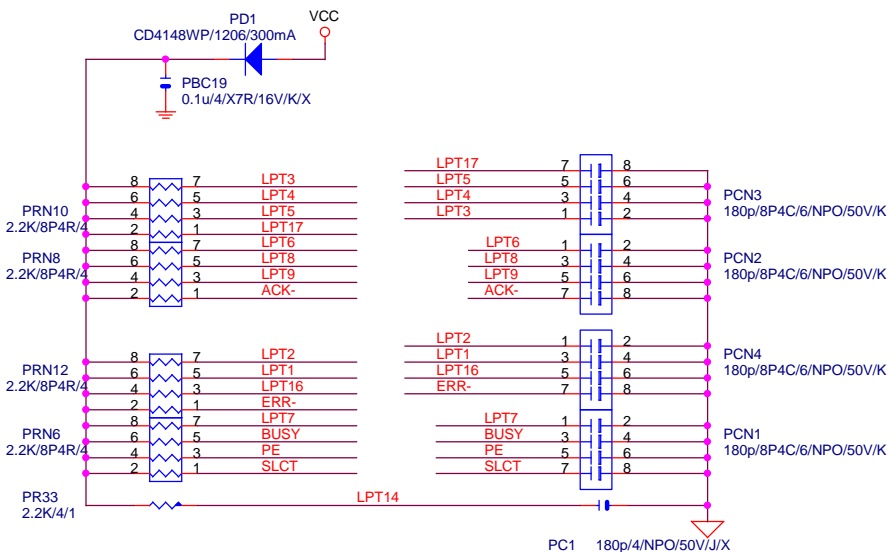
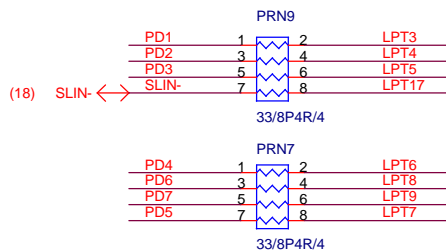
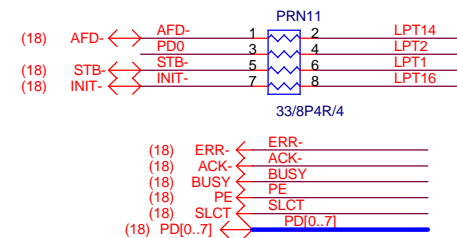


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GA-H61M-S2P-GQ			
Size	Document Number	Date	Rev
Custom	Friday, July 06, 2012	31	1.0

TPM

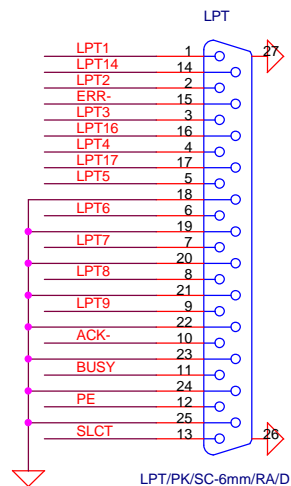
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Title			LPT
Document Number			GA-H61M-S2P-GQ
Size Custom	Date: Friday, July 06, 2012	Sheet 32 of 32	Rev 1.0